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Register Map – MODBUS ADDRESS DIGITAL VOLTAGE REGULATOR RELAY – DVR – RELAY 90 ANSI

SERIAL COMMUNICATION

Protocol: MODBUS ADDRESS

Baud Rate: 2400 to 57.600 bps (Auto Baud Rate)

Data bits: 8 bits

Parity: None/Even/Odd;

Variable Type: Holding Register (40.000)

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale	
		-		Register – Phase Regulation:			
	1 to 6	-	1	Phase Regulation Set A A;	W / R	-	
		-	2	Phase Regulation Set B B;	W / R	-	
1		1 to 6	1 to 6	-	3	Phase Regulation Set C C;	W / R
		_	4	Phase Regulation Set AB A;	W / R	-	
		_	5	Phase Regulation Set AB B;	W / R	-	
		-	6	Phase Regulation Set AB C;	W / R	-	
		-		Register – Regulation Operation mode	1		
		-	0	Automatic;	W / R	-	
2	1 + - 1	-	1	Local Operation;	W / R	-	
	2 1 to 4	-	2	Remote Operation;	W / R	-	
		-	3	Local/Remote Operation;	W / R	-	
		-	4	Blockage Operation;	W / R	-	

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MODBUS Address	Reading Range	Bits Index	State	Point Name		Write Read	Scale
		-		Register – Enable and/or Disable Regulation Set.		_	
		0	0	Disable Regulation Set 1;		W / R	_
		0	1	Enable Regulation Set 1;		W / R	_
		1	0	Disable Regulation Set 2;		W / R	-
		1	1	Enable Regulation Set 2;		W / R	-
		2	0	Disable Regulation Set 3;		W / R	-
			1	Enable Regulation Set 3;		W / R	_
		3	0	Disable Regulation Set 4;		W / R	-
2		5	1	Enable Regulation Set 4;		W / R	-
3	_	- 4	0	Disable Regulation Set 5;		W / R	-
		4	1	Enable Regulation Set 5;		W / R	-
		5	0	Disable Regulation Set 6;		W / R	-
		5	1	Enable Regulation Set 6;		W / R	-
		6	0	Disable Regulation Set 7;		W / R	_
		0	1	Enable Regulation Set 7;		W / R	_
		7	0	Disable Regulation Set 8;		W / R	-
		,	1	Enable Regulation Set 8;		W / R	-
10	400 to 2800	-		Rated Voltage	Reg.Set.1	W / R	-
11	1 to 100	-		Maximum Deviation Percent Step 1;	Reg.Set.1	W / R	1:10
12	0 to 100	-		Maximum Deviation Percent Step 2;	Reg.Set.1	W / R	1:10
13	0 to 100	-		Maximum Deviation Percent Step 3;	Reg.Set.1	W / R	1:10
		-		Register – Timing Type	Reg.Set.1	-	
14	14 0 to 2 –	0	Inverse timing;	Reg.Set.1	W / R	_	
<u> </u>		-	1	Linear Timing;	Reg.Set.1	W / R	_
		_	2	Step timing;	Reg.Set.1	W / R	_

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MODBUS Address	Reading Range	Bits Index	State	Point Name		Write Read	Scale
15	0 to 180	_		Time to raise step 1 (seconds);	Reg.Set.1	W / R	1:1
16	0 to 180	-		Time to raise step 2 (seconds);	Reg.Set.1	W / R	1:1
17	0 to 180	-		Time to raise step 3 (seconds);	Reg.Set.1	W / R	1:1
18	0 to 180	-		Time to lower step 1 (seconds);	Reg.Set.1	W / R	1:1
19	0 to 180	-		Time to lower step 2 (seconds);	Reg.Set.1	W / R	1:1
20	0 to 180	-		Time to lower step 3 (seconds);	Reg.Set.1	W / R	1:1
21	0 to 180	-		Subsequent Time	Reg.Set.1	W / R	1:1
	0 to 180	_		Register – Line Fall Compensation Time – LDC	Reg.Set.1	_	
22	0 to 180	-	0	Compensation – Z;	Reg.Set.1	W / R	_
	0 to 180	-	1	Compensation – RX;	Reg.Set.1	W / R	_
23	0 to 500	_		Line fall resistive component; – Volts;	Reg.Set.1	W / R	-250:10
24	0 to 500	_		Line fall reactive component; – Volts;	Reg.Set.1	W / R	-250:10
25	0 to 150	_		Line fall percentage – Z Compensation;	Reg.Set.1	W / R	1:10
26	1 to 250	-		Maximum line percentage compensation;	Reg.Set.1	W / R	1:10
40	400 to 2800	-		Ratio Voltage	Reg.Set.2	W / R	1:10
41	1 to 100	-		Step 1 maximum percentage deviation;	Reg.Set.2	W / R	1:10
42	1 to 100	-		Step 2 maximum percentage deviation;	Reg.Set.2	W / R	1:10
43	1 to 100	_		Step 3 maximum percentage deviation;	Reg.Set.2	W / R	1:10
		_		Register – Timing type.	Reg.Set.2	_	
44	0 to 2	-	0	Inverse timing;	Reg.Set.2	W / R	-
••	0.02	_	1	Linear timing;	Reg.Set.2	W / R	-
		—	2	Step Timing;	Reg.Set.2	W / R	-
45	0 to 180	_		Step 1 raising time (seconds);	Reg.Set.2	W / R	1:1
46	0 to 180	_		Step 2 raising time (seconds);	Reg.Set.2	W / R	1:1
47	0 to 180	_		Step 3 raising time (seconds);	Reg.Set.2	W/R	1:1

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SERIAL COMMUNICATION

Write **Reading Address** MODBUS Address **Bits Index** State **Description / Point Name** Scale Read W/R 48 0 to 180 Step 1 lower time (seconds); Reg.Set.2 1:1 _ Step 2 lower time (seconds); Reg.Set.2 49 0 to 180 W/R 1:1 _ 50 0 to 180 Step 3 lower time (seconds); Reg.Set.2 W/R 1:1 _ W/R 51 Subsequent time (Seconds); 1:1 0 to 30 _ Reg.Set.2 Register – Line Fall Compensation Time – LDC Reg.Set.2 _ _ 52 0 to 1 Compensation – Z; Reg.Set.2 W/R _ 0 _ Compensation – RX; Reg.Set.2 W/R 1 _ _ Line fall resistive componente – Volts; W/R 0 to 500 Reg.Set.2 -250:10 53 _ Line fall reactive componente – Volts; Reg.Set.2 W/R 54 0 to 500 _ -250:10 Line fall percentage – Compensation Z; 55 0 to 150 Reg.Set.2 W/R 1:10 — 56 W/R 1 to 250 Maximum compensation percentage. Reg.Set.2 1:10 _ 57 0 to 23 Reg.Set.2 W/R 1:1 Regulation starting hour; _ W/R 58 0 to 59 Regulation starting minute; Reg.Set.2 1:1 _ 59 Reg.Set.2 1:1 0 to 23 Regulation final hour; W/R — W/R 60 0 to 59 Regulation final minute; Reg.Set.2 1:1 _ Register – Regulation day; Reg.Set.2 W/R 1:1 _ Regulation Set – Daily; W/R 0 Reg.Set.2 _ _ Regulation Set – Sunday; Reg.Set.2 W/R 1 _ _ W/R 2 Regulation Set – Monday; Reg.Set.2 _ _ 61 0 to 7 3 Regulation Set – Tuesday; Reg.Set.2 W/R _ W/R _ 4 Regulation Set – Wednesday; Reg.Set.2 _ Regulation Set – Thursday; W/R 5 Reg.Set.2 — _ W/R 6 Regulation Set – Friday; Reg.Set.2 _ _ Regulation Set – Saturday; Reg.Set.2 7 W/R _ _

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MODBUS address	Reading Range	Bits Index	State	Description / Point Name		Write Read	Scale
		_		Register – Hour Calendar	Reg.Set.2		_
62	0 to 1	-	0	Turn off Regulation Set by the Hour and Calendar;	Reg.Set.2	W / R	-
		_	1	Turn on Regulation Set by the Hour and Calendar;	Reg.Set.2	W / R	_
70	400 to 2800	_		Rated Voltage;	Reg.Set.3		_
71	1 to 100	_		Maximum Deviation Percentage – Step 1;	Reg.Set.3	W / R	_
72	0 to 100	_		Maximum Deviation Percentage – Step 2;	Reg.Set.3	W / R	_
73	0 to 100	-		Maximum Deviation Percentage – Step 3;	Reg.Set.3	W / R	_
		-		Register – Timing type;	Reg.Set.3	W / R	-
74	0 to 2		0	Inverse Timing;	Reg.Set.3	W / R	-
	0.00 -		1	Linear Timing;	Reg.Set.3	W / R	-
			2	Step Timing;	Reg.Set.3	W / R	-
75	0 to 180	-		Step 1 raising time (Seconds);	Reg.Set.3	W / R	1:1
76	0 to 180	_		Step 2 raising time (Seconds);	Reg.Set.3	W / R	1:1
77	0 to 180	-		Step 3 raising time (Seconds);	Reg.Set.3	W / R	1:1
78	0 to 180	-		Step 1 lower time (Seconds);	Reg.Set.3	W / R	1:1
79	0 to 180	-		Step 2 lower time (Seconds);	Reg.Set.3	W / R	1:1
80	0 to 180	-		Step 3 lower time (Seconds);	Reg.Set.3	W / R	1:1
81	0 to 30	-		Subsequent time (Seconds);	Reg.Set.3	W / R	1:1
		-		Register – Line fall Compensation Time – LDC ;	Reg.Set.3		_
82	0 to 1	0		Compensation – Z;	Reg.Set.3	W / R	_
		1		Compensation – RX;	Reg.Set.3	W / R	-
83	0 to 500	_		Line fall resistance component – Volts;	Reg.Set.3	W / R	-250:10
84	0 to 500	_		Line fall reactive component – Volts;	Reg.Set.3	W / R	-250:10
85	0 to 150	_		Line fall percentage – Z Compensation;	Reg.Set.3	W / R	1:10
86	0 to 250	-		Maximum compensation percentage;	Reg.Set.3	W/R	1:10



SERIAL COMMUNICATION

Write **Reading Range** MODBUS address Bits Index **Description / Point Name** State Scale Read W/R 87 0 to 23 **Regulation Starting Hour;** Reg.Set.3 1:1 _ W/R 88 **Regulation Starting Minute;** Reg.Set.3 1:1 0 to 59 _ 89 0 to 23 Regulation Final Hour; Reg.Set.3 W/R 1:1 _ **Regulation Final Minute;** Reg.Set.3 W/R 90 0 to 59 1:1 _ Register – Regulation day. Reg.Set.3 W/R 1:1 _ Regulation Set – Daily; Reg.Set.3 W/R 0 _ _ 1 Regulation Set – Sunday; Reg.Set.3 W/R _ _ Regulation Set – Monday; W/R Reg.Set.3 2 _ — 91 0 to 7 3 Regulation Set – Tuesday; Reg.Set.3 W/R _ _ 4 Regulation Set – Wednesday; Reg.Set.3 W/R _ _ Regulation Set – Thursday; 5 Reg.Set.3 W/R _ _ Regulation Set – Friday; 6 Reg.Set.3 W/R _ _ 7 Regulation Set – Saturday; W/R Reg.Set.3 _ _ Register – Hour Calendar Reg.Set.3 92 0 to 1 Turn on Regulation Set by the Hour and Calendar; W/R 0 Reg.Set.4 _ _ Turn on Regulation Set by the Hour and Calendar; Reg.Set.4 1 W/R _ — 100 400 to 2800 Reg.Set.4 W/R Rated Voltage; _ _ 101 Maximum Deviation Percentage – Step 1; Reg.Set.4 W/R 1 to 100 _ _ W/R 102 Maximum Deviation Percentage – Step 2; 0 to 100 Reg.Set.4 _ _ Maximum Deviation Percentage – Step 3; W/R 103 Reg.Set.4 0 to 100 _ _ Register – Timing type; Reg.Set.4 W/R _ _ Inverse Timing; Reg.Set.4 W/R 0 _ _ 104 0 to 2 Linear Timing; W/R 1 Reg.Set.4 _ _ 2 Reg.Set.4 W/R Step Timing; _ _ Step 1 raising time (Seconds); W/R 105 0 to 180 _ Reg.Set.4 1:1

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SERIAL COMMUNICATION

Write **Reading Range** MODBUS address Bits Index **Description / Point Name** State Scale Read 0 to 180 Step 2 Raise Time; (Seconds) Reg.Set.4 W/R 106 1:1 _ W/R 107 Step 3 Raise Time; (Seconds) Reg.Set.4 1:1 0 to 180 _ 108 0 to 180 Step 1 Lower Time; (Seconds) Reg.Set.4 W/R 1:1 _ Step 2 Lower Time; (Seconds) Reg.Set.4 109 0 to 180 W/R 1:1 _ 110 0 to 180 Step 3 Lower Time; (Seconds) Reg.Set.4 W/R 1:1 _ Subsequent Time; (Seconds) Reg.Set.4 W/R 1:1 111 0 to 30 _ Register – Line fall compensation time – LDC Reg.Set.4 _ _ 112 0 to 1 Compensation – Z: 0 Reg.Set.4 W/R — 1 Compensation – RX; Reg.Set.4 W/R _ 0 to 500 Line fall resistance component – Volts; W / R 113 _ Reg.Set.4 -250:10Line fall reactive component – Volts; Reg.Set.4 W/R 114 0 to 500 _ -250:10 Line fall percentage – Z Compensation; 0 to 150 115 Reg.Set.4 W/R 1:10 _ 116 1 to 250 Maximum compensation percentage; Reg.Set.4 W/R 1:10 _ 117 0 to 23 **Regulation Starting Hour;** Reg.Set.4 W/R 1:1 118 0 to 59 **Regulation Starting Minute;** Reg.Set.4 W/R 1:1 _ Regulation Final Hour; Reg.Set.4 119 0 to 23 W/R 1:1 _ 120 0 a 39 W/R **Regulation Final Minute;** Reg.Set.4 1:1 _ Register – Regulation day.; Reg.Set.4 W/R 1:1 W/R Regulation Set – Daily; _ 0 Reg.Set.4 _ Regulation Set – Sunday; W/R Reg.Set.4 1 _ _ 121 2 Regulation Set – Monday; Reg.Set.4 W/R 0 to 7 _ _ W/R 3 Regulation Set – Tuesday; Reg.Set.4 _ _ Regulation Set – Wednesday; W/R 4 Reg.Set.4 _ _ 5 Regulation Set – Thursday; Reg.Set.4 W/R — _ 6 W/R _ Regulation Set – Friday; Reg.Set.4 _

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MODBUS address	Reading Range	Bits Index	State	Description / Point Name		Write Read	Scale
121	0 to 7	_	7	Regulation Set – Saturday; (Seconds)	Reg.Set.4	W/R	_
		-		Register – Timing type;	Reg.Set.5		_
122	0 to 2	_	0	Turn Off Regulation Set by the Hour and Calendar;	Reg.Set.5	W / R	—
		_	1	Turn on Regulation Set by the Hour and Calendar;	Reg.Set.5	W / R	—
130	400 to 2800	-		Rated Voltage;	Reg.Set.5	W / R	1:10
131	1 to 100	-		Maximum Deviation Percentage – Step 1;	Reg.Set.5	W / R	1:10
132	0 to 100	-		Maximum Deviation Percentage – Step 2;	Reg.Set.5	W / R	1:10
133	0 to 100	-		Maximum Deviation Percentage – Step 3;	Reg.Set.5	W / R	1:10
		-		Register – Timing type;	Reg.Set.5		_
134	0 to 2	0		Inverse Timing;	Reg.Set.5	W / R	_
101	0.00 2	1		Linear Timing;	Reg.Set.5	W / R	_
		2		Step Timing;	Reg.Set.5	W / R	_
135	0 to 180	_		Step 1 raising time (Seconds);	Reg.Set.5	W / R	1:1
136	0 to 180	-		Step 2 raising time (Seconds);	Reg.Set.5	W / R	1:1
137	0 to 180	-		Step 3 raising time (Seconds);	Reg.Set.5	W / R	1:1
138	0 to 180	-		Step 1 lower time (Seconds);	Reg.Set.5	W / R	1:1
139	0 to 180	-		Step 2 lower time (Seconds);	Reg.Set.5	W / R	1:1
140	0 to 180	_		Step 3 lower time (Seconds);	Reg.Set.5	W / R	1:1
141	0 to 180	-		Subsequent time (Seconds);	Reg.Set.5	W / R	1:1
		-		Register – Regulation day.	Reg.Set.5		_
142	0 to 1	_	0	Compensation – Z;	Reg.Set.5	W / R	-
		-	1	Compensation – RX;	Reg.Set.5	W / R	-
143	0 to 500	-		Line fall resistive component – Volts;	Reg.Set.5	W / R	-250:10
144	0 to 500	-		Line fall reactive component – Volts;	Reg.Set.5	W / R	-250:10
145	0 to 150	-		Line fall percentage – Z Compensation;	Reg.Set.5	W/R	1:10

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MODBUS address	Reading Range	Bits Index	State	Description / Point Name		Write Read	Scale
146	1 to 250	_	I	Maximum compensation percentage;	Reg.Set.5	W / R	1:10
147	0 to 23	_		Regulation Starting Hour;	Reg.Set.5	W/R	1:1
148	0 to 59	_		Regulation Starting Minute;	Reg.Set.5	W/R	1:1
149	0 to 23	_		Regulation Final Hour;	Reg.Set.5	W/R	1:1
150	0 to 59	_		Regulation Final Minute;	Reg.Set.5	W/R	1:1
		-		Register – Regulation day.	Reg.Set.5	-	_
		_	0	Regulation Set – Daily;	Reg.Set.5	W/R	-
		_	1	Regulation Set – Sunday;	Reg.Set.5	W/R	—
151	0 to 7	_	2	Regulation Set – Monday;	Reg.Set.5	W/R	-
101	0107	-	3	Regulation Set – Tuesday;	Reg.Set.5	W/R	-
		-	4	Regulation Set – Wednesday;	Reg.Set.5	W/R	_
		-	5	Regulation Set – Thursday;	Reg.Set.5	W/R	_
		-	6	Regulation Set – Friday;	Reg.Set.5	W/R	_
		-	7	Regulation Set – Sunday;	Reg.Set.5	W/R	-
		-		Register – Hour Calendar	Reg.Set.6	W/R	-
152	0 to 2	-	0	Rated Voltage;	Reg.Set.5	W/R	-
102	0102	-	1	Shut down Regulation Set by the Hour;	Reg.Set.5	W/R	_
		-	2	Turn on Regulation Set by the Hour and Calendar;	Reg.Set.5	W/R	_
160	400 to 2800	_		Rated Voltage;	Reg.Set.6	W/R	1:10
161	1 to 100	_		Maximum Deviation Percentage – Step 1;	Reg.Set.6	W / R	1:10
162	0 to 100	-		Maximum Deviation Percentage – Step 2;	Reg.Set.6	W / R	1:10
163	0 to 100	-		Maximum Deviation Percentage – Step 3;	Reg.Set.6	W / R	1:10
		-		Register – Timing type;	Reg.Set.6	-	_
164	0 to 2	-	0	Inverse Timing;	Reg.Set.6	W / R	_
			1	Linear Timing;	Reg.Set.6	W/R	_

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MODBUS address	Reading Range	Bits Index	State	Description / Point Name		Write Read	Scale
164	0 to 2	_	2	Step Timing;	Reg.Set.6	W / R	1:1
165	0 to 180	-	_	Step 1 raising time (Seconds);	Reg.Set.6	W / R	1:1
166	0 to 180	-	_	Step 2 raising time (Seconds);	Reg.Set.6	W / R	1:1
167	0 to 180	-	_	Step 3 raising time (Seconds);	Reg.Set.6	W / R	1:1
168	0 to 180	-	_	Step 1 lower time (Seconds);	Reg.Set.6	W / R	1:1
169	0 to 180	-	_	Step 2 lower time (Seconds);	Reg.Set.6	W / R	1:1
170	0 to 180	-	-	Step 3 lower time (Seconds);	Reg.Set.6	W / R	1:1
171	0 to 180	-	-	Subsequent time (Seconds);	Reg.Set.6	W / R	1:1
		-	-	Register – Line fall compensation time – LDC	Reg.Set.6		-
172	0 to 1	-	0	Compensation – Z	Reg.Set.6	W / R	—
		-	1	Compensation – RX	Reg.Set.6	W / R	
173	0 to 500	-	_	Line fall resistance component – Volts;	Reg.Set.6	W / R	-250:10
174	0 to 500	-	_	Line fall reactive component – Volts;	Reg.Set.6	W / R	-250:10
175	0 to 150	-	-	Line fall component – Z Compensation;	Reg.Set.6	W / R	1:10
176	1 to 250	-	-	Maximum compensation percentage;	Reg.Set.6	W / R	1:10
177	0 to 23	-	-	Regulation Starting Hour;	Reg.Set.6	W / R	1:1
178	0 to 59	-	-	Regulation Starting Minute;	Reg.Set.6	W / R	1:1
179	0 to 23	-	_	Regulation Final Hour;	Reg.Set.6	W / R	1:1
180	0 to 59	-	-	Regulation Final Minute;	Reg.Set.6	W / R	1:1
		-	-	Register – Regulation day.;	Reg.Set.6	W / R	1:1
		-	0	Regulation Set – Daily;	Reg.Set.6	1:1	-
181	0 to 7	_	1	Regulation Set – Sunday;	Reg.Set.6	1:1	-
		-	2	Regulation Set – Monday;	Reg.Set.6	W / R	
		_	3	Regulation Set – Tuesday;	Reg.Set.6	W / R	
		-	4	Regulation Set – Wednesday;	Reg.Set.6	W / R	—

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MODBUS address	Reading Range	Bits Index	State	Description / Point Name		Write Read	Scale
		-	5	Regulation Set – Thursday;	Reg.Set.6	W / R	_
181	0 to 7	-	6	Regulation Set – Friday;	Reg.Set.6	W / R	_
		-	7	Regulation Set – Sunday;	Reg.Set.6	W / R	—
		-	-	Register – Hour Calendar	Reg.Set.6		-
182	0 to 1	-	0	Turn on Regulation Set by the Hour and Calendar;	Reg.Set.6	W / R	_
		-	1	Turn on Regulation Set by the Hour and Calendar;	Reg.Set.6	W / R	_
190	400 to 2800	-	-	Rated Voltage;	Reg.Set.7	W / R	-
191	1 to 100	-	-	Maximum Deviation Percentage – Step 1;	Reg.Set.7	W / R	-
192	1 to 100	-	-	Maximum Deviation Percentage – Step 2;	Reg.Set.7	W / R	-
193	1 to 100	-	_	Maximum Deviation Percentage – Step 3;	Reg.Set.7	W / R	_
		-	-	Register – Timing type;	Reg.Set.7	W / R	_
194	0 to 2	_	0	Inverse Timing;	Reg.Set.7	W/R	_
191	0102	-	1	Linear Timing;	Reg.Set.7	W / R	_
		_	2	Step Timing;	Reg.Set.7	W / R	_
195	0 to 180	-	_	Step 1 raising time (Seconds);	Reg.Set.7	W / R	1:1
196	0 to 180	-	-	Step 2 raising time (Seconds);	Reg.Set.7	W / R	1:1
197	0 to 180	-	-	Step 3 raising time (Seconds);	Reg.Set.7	W / R	1:1
198	0 to 180	-	_	Step 1 lower time (Seconds);	Reg.Set.7	W / R	1:1
199	0 to 180	-	_	Step 2 lower time (Seconds);	Reg.Set.7	W/R	1:1
200	0 to 180	-	_	Step 3 lower time (Seconds);	Reg.Set.7	W/R	1:1
201	0 to 30	-	_	Subsequent time (Seconds);	Reg.Set.7	W / R	1:1
		-	_	Register – Line fall Compensation Time – LDC	Reg.Set.7		_
202	0 to 1	_	0	Compensation – Z;	Reg.Set.7	W / R	-
		_	1	Compensation – RX;	Reg.Set.7	W / R	-
203	0 to 300	-	_	Line fall resistive component – Volts;	Reg.Set.7	W/R	-250:10

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MODBUS address	Reading Range	Bits Index	State	Description / Point Name		Write Read	Scale
204	0 to 300	-	-	Line fall reactive component – Volts	Reg.Set.7	W / R	-250:10
205	0 to 300	-	-	Line fall percentage – Z Compensation	Reg.Set.7	W / R	1:10
206	0 to 300	-	_	Maximum compensation percentage;	Reg.Set.7	W / R	1:10
207	0 to 300	-	-	Regulation Starting Hour;	Reg.Set.7	W / R	1:1
208	0 to 300	-	-	Regulation Starting Minute;	Reg.Set.7	W / R	1:1
209	0 to 300	-	_	Final Hour Regulation;	Reg.Set.7	W / R	1:1
210	0 to 300	-	_	Minuto Final da Regulação.	Reg.Set.7	W / R	1:1
		-	_	Register – Regulation day;	Reg.Set.7	W / R	1:1
		-	0	Regulation Set – Daily;	Reg.Set.7	W / R	-
		_	1	Regulation Set – Sunday;	Reg.Set.7	W / R	-
211	0 to 7	_	2	Regulation Set – Monday;	Reg.Set.7	W / R	-
211	0107	_	3	Regulation Set – Tuesday;	Reg.Set.7	W / R	-
		_	4	Regulation Set – Wednesday;	Reg.Set.7	W / R	-
		_	5	Regulation Set – Thursday;	Reg.Set.7	W / R	_
		_	6	Regulation Set – Friday;	Reg.Set.7	W / R	_
		_	7	Regulation Set – Sunday;	Reg.Set.7	W / R	_
		-	_	Register – Hour Calendar	Reg.Set.7	W / R	_
212	0 to 1	_	0	Turn Off Regulation Set by the Hour and Calendar;	Reg.Set.7	W / R	_
		_	1	Turn on Regulation Set by the Hour and Calendar;	Reg.Set.7	W / R	_
		-	_	Register – Turn on Regulation Set by the Hour and Calendar;	_	-	_
		0	1	Intermediate Position 1– Enabled;		W / R	_
214	_	1	1	Intermediate Position 2 – Enabled;		W / R	_
		2	1	Intermediate Position 3 – Enabled;		W / R	_
		3	1	Intermediate Position 4 – Enabled;		W / R	_
		4	1	Intermediate Position 5 – Enabled;		W/R	-

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MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
215	-50 to 50	-	-	Intermediate Position Initial Percentage 1;	W / R	-100
216	-50 to 50	-	_	Intermediate Position Initial Percentage 2;	W / R	-100
217	-50 to 50	-	-	Intermediate Position Initial Percentage 3;	W / R	-100
218	-50 to 50	-	-	Intermediate Position Initial Percentage 4;	W / R	-100
219	-50 to 50	-	-	Intermediate Position Initial Percentage 5;	W / R	-100
220	400 to 2800	-	_	Rated Voltage; Reg.Set.8	W / R	-100
221	1 to 100	-	-	Maximum Deviation Percentage – Step 1; Reg.Set.8	W / R	1:10
222	0 to 100	-	_	Maximum Deviation Percentage – Step 2; Reg.Set.8	W / R	1:10
223	0 to 100	-	_	Maximum Deviation Percentage – Step 3; Reg.Set.8	W / R	1:10
		-	_	Register – Timing type; Reg.Set.8		_
224	0 to 2	-	0	Inverse Timing; Reg.Set.8	W / R	_
	0.00 -	-	1	Linear Timing; Reg.Set.8	W / R	_
		-	2	Step Timing; Reg.Set.8	W / R	_
225	0 to 180	-	-	Step 1 raising time (Seconds); Reg.Set.8	W / R	1:1
226	0 to 180	-	-	Step 2 raising time (Seconds); Reg.Set.8	W / R	1:1
227	0 to 180	-	-	Step 3 raising time (Seconds); Reg.Set.8	W / R	1:1
228	0 to 180	-	-	Step 1 lower time (Seconds); Reg.Set.8	W / R	1:1
229	0 to 180	-	_	Step 2 lower time (Seconds); Reg.Set.8	W / R	1:1
230	0 to 180	-	-	Step 3 lower time (Seconds); Reg.Set.8	W / R	1:1
231	0 to 30	-	-	Subsequent time (Seconds); Reg.Set.8	W / R	1:1
		-	-	Register – Line fall Compensation Time – LDC;Reg.Set.8		_
232	0 to 1	-	0	Compensation – Z; Reg.Set.8	W / R	—
		-	1	Compensation – RX; Reg.Set.8	W / R	_
233	0 to 500	-	-	Line fall resistance component – Volts; Reg.Set.8		-250:10
234	0 to 500	-	-	Line fall reactive component – Volts Reg.Set.8		-250:10

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name		Write / Read	Scale
235	0 to 150	-		Intermediate Position Initial Percentage 1;	Reg.Set.8	W / R	1:10
236	1 to 250	-		Maximum compensation percentage;	Reg.Set.8	W / R	1:10
237	0 to 23	-		Regulation Starting Hour;	Reg.Set.8	W / R	1:1
238	0 to 59	-		Regulation Starting Minute;	Reg.Set.8	W / R	1:1
239	0 to 23	-		Regulation Final Hour;	Reg.Set.8	W / R	1:1
240	0 to 59	-		Regulation Final Number;	Reg.Set.8	W / R	1:1
		-		Register – Regulation day.	Reg.Set.8	W / R	1:1
		_	0	Regulation Set – Daily;	Reg.Set.8	W / R	_
		_	1	Regulation Set – Sunday;	Reg.Set.8	W / R	_
2.4.1	241 0 to 7	_	2	Regulation Set – Monday;	Reg.Set.8	W / R	_
241 0 to 7	0107	_	3	Regulation Set – Tuesday;	Reg.Set.8	W / R	_
		_	4	Regulation Set – Wednesday;	Reg.Set.8	W / R	_
		_	5	Regulation Set – Thursday;	Reg.Set.8	W / R	-
		_	6	Regulation Set – Friday;	Reg.Set.8	W / R	_
		_	7	Regulation Set – Saturday;	Reg.Set.8	W / R	_
		_		Register – Hour Calendar	Reg.Set.8	_	
242	0 to 1	_	0	Turn Off Regulation Set by the Hour and Calendar;	Reg.Set.8	W / R	-
		_	1	Turn On Regulation Set by the Hour and Calendar;	Reg.Set.8	W / R	-
245	1 to 10	-		Operations Numbers of Intermediate Position 1;		W / R	1:1
246	1 to 10	-		Operations Numbers of Intermediate Position 2;		W / R	1:1
247	1 to 10	-		Operations Numbers of Intermediate Position 3;		W / R	1:1
248	1 to 10	-		Operations Numbers of Intermediate Position 4;		W / R	1:1
249	1 to 10	-		Operations Numbers of Intermediate Position 5;		W / R	1:1
250	10 to 99	-		Undervoltage Block Percentage;		W / R	1:1
251	0 to 250	_		Undervoltage Hysteresis Percentage;		W/R	1:10



SERIAL COMMUNICATION

Bits Write **Reading Range MODBUS address Description / Point Name** State Scale Index Read W/R 252 0 to 1200 Undervoltage time delay blockage; 1:1 _ 101 to 199 W/R 253 Overvoltage blockage percentage; 1:1 _ 254 0 to 250 Overvoltage blockage hysteresis; W/R 1:10 _ Delay time for overvoltage blocking; W/R 255 0 to 1200 1:1 _ 256 101 to 199 Over Current blockage percentage; W/R 1:1 _ 257 Over Current Hysteresis; W/R 1:10 0 to 250 _ 258 0 to 1200 _ Delay time for overcurrent blocking; W/R 1:1 Register – Phase Reading of Electrical Current; _ _ Phase Current Reading A Disable; W/R 0 _ 0 1 Phase Current Reading A Enabled; W/R _ 260 W/R Phase Current Reading B Disabled; 0 _ 1 Phase Current Reading B Enabled; W/R 1 _ Phase Current Reading C Disabled; W/R 0 _ 2 Phase Current Reading C Enabled; W/R 1 _ Register – Phase Reading of Electrical Current; W/R _ _ Current Reading Phase A Disabled; 0 _ 0 1 Current Reading Phase A Enabled; W/R _ 261 Current Reading Phase B Disabled; W/R 0 _ 1 W/R 1 Current Reading Phase B Enabled; _ Current Reading Phase C Disabled; W/R 0 _ 2 1 Current Reading Phase C Enabled; W/R _ Rated Winding Current 1; W/R 262 1 to 9999 1:1000 W/R Rated Winding Current 2; 263 1 to 9999 _ 1:1000 264 1 to 9999 Rated Winding Current 3; W/R 1:1000 _ W/R 265 1 to 9999 _ PT Ratio Phase A; 1:1

Electron

SERIAL COMMUNICATION

Bits Write **Reading Range** MODBUS address **Description / Point Name** State Scale Index Read W/R 266 1 to 9999 PT Ratio Phase B; 1:1 _ W/R 267 PT Ratio Phase C: 1:1 1 to 9999 _ 268 1 to 9999 Winding 1 CT Ratio; W/R 1:1 _ Winding 2 CT Ratio; W/R 269 1 to 9999 1:1 _ 270 1 to 9999 Winding 3 CT Ratio; W/R 1:1 _ _ Register – Squared Current Sum Alarm; _ 275 0.01 to Squared Current Sum Alarm – LSB; W/R _ _ 1:1000 99999.99 Squared Current Sum Alarm – MSB: W/R 276 _ — Register – Reference basis for maintenance warning – Electrical Current; W/R _ _ 0 Reference basis for maintenance warning – Daily; W/R _ Reference basis for maintenance warning – Weekly; W/R 1 _ _ 277 0 to 5 Reference basis for maintenance warning – Monthly; W/R 2 _ _ 3 W/R Reference basis for maintenance warning – Quarterly; _ _ W/R 4 Reference basis for maintenance warning – Semesterly; _ W/R 5 Reference basis for maintenance warning – Annual; _ _ Number of days to notice; 278 1 - 9999W/R 1:1 — Register – Calculation basis for maintenance; W/R _ _ 279 0 to 1 Calculation over last operations; W/R _ 0 _ 1 Total Calculation over TAP Changer last operations; W/R _ _ Resistive Step; W/R 280 40 to 1000 1:10 _ Register – Initial Crown Step: _ 281 0 to 1 Starts from Zero Ohm; W/R 0 _ _ W/R Starts from Resistive Step; _ 1 _ Initial Position of Potentiometric Crown; 282 50 to 151 W/R - 100 _ W/R 283 50 to 151 _ Neutral Position of Potentiometric Crown; - 100



SERIAL COMMUNICATION

Write Bits **Reading Range** MODBUS address State **Description / Point Name** Scale Index Read Final Position of Potentiometric Crown; W/R 284 50 to 151 -100_ Register – Potentionetric Crown Indication Mode; 285 0 to 1 0 Numeric Mode: W/R _ _ W/R Alphanumeric Mode; 1 _ Commutation Time (Seconds); 286 1 to 100 1:1 _ _ Register – TAP Changer blockage W/R 0 Undervoltage Blocking Disabled; _ 0 Undervoltage Blocking Enabled; W/R 1 _ W/R Overvoltage Blockage Disabled; 0 _ 1 W/R 1 Overvoltage Blockage Enabled; _ 287 W/R Over current Blockage Disabled; 0 _ 2 **Over Current Blockage Enabled;** W/R 1 _ Reverse Current Blockage Disable; W/R 0 _ 5 Reverse Current Blockage Enabled; W/R 1 W/R Commutation Failure Blockage Disabled; 0 _ 6 W/R 1 Commutation Failure Blockage Enabled; _ Register – Reference basis for maintenance warning – Commutation; _ _ W/R Reference basis for maintenance warning – Daily; 0 _ _ W/R 1 Reference basis for maintenance warning – Weekly; _ _ 288 0 to 5 Reference basis for maintenance warning – Monthly; W/R 2 _ _ W/R 3 Reference basis for maintenance warning – Quarterly; _ _ Reference basis for maintenance warning – Semesterly; W/R 4 _ _ W/R 5 Reference basis for maintenance warning – Annual; _ _ 289 1 - 9999Time numbers for TAP Changer Operation Numbers; W/R 1:1 _ Register – Commutations Numbers for TAP Changer Maintenance

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
290	0 to 16x10 ⁶	-	-	Less Significative Value – TAP Changer Maintenance;	W / R	1:1
291	01010X10	_	-	Most Significative Value – TAP Changer Maintenance;	W / R	1:1
292	1	-	-	Register – Raise TAP Changer Voltage Command;	-	_
		-	1	Execute TAP Changer Raise Command;	R	-
293	1	-	_	Register – TAP Changer Raise Voltage Command;	-	_
293	1	1	1	Execute TAP Changer Raise Voltage Command;	R	_
		-	-	Register – Sucessive Command type to TAP Changer.	-	_
294	0 to 1	-	0	TAP Changer Blockage;	W / R	—
		-	1	Return Previous Position and TAP Changer blockage;	W / R	_
		-	_	Register – Parallelism Type;	W / R	_
		1	0	Parallelism Follower type mode (Slave);	W / R	_
300	0 a 3	I	1	Parallelism Master type mode (Master);	W / R	_
		I	2	Parallelism in individual mode;	W / R	_
		-	3	Parallelism mode off;	W / R	_
301	0 to 0xFFFF	-	_	Equipment number on three–phase network;	W / R	1:1
302	0 to 0xFFFF	-	_	Equipment number on Transformer Bank;	W / R	1:1
303	0 to 255	-	-	Equipment address on Parallelism Network;	W / R	1:1
		_	-	Register – Parallel Network Topology;	_	_
304	0 to 1	-	0	Parallel Network Topology mode off;	-	-
		-	1	Parallel Network Topology mode on;	_	_
305	0 to 0xFFFF	-	-	Register – Equipment Status 1 to 16 in Parallel Network;	W / R	1:1
306	0 to 0xFFFF	-	-	Register – Equipment Status 17 to 31 in Parallel Network;	W / R	1:1
310	0 to 255	-	-	OLED Display Contrast;	W / R	1:1
311	0 to 1	-	-	Register – Display Write Mode;		_
511	0.01	_	0	Black Display and White Write;	W / R	_

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
311	0 to 1	_	1	White Display and Black Write;	W/R	_
		-	_	Register – Stand Alone de Regulação;	-	_
312	0 to 1	-	0	Stand Alone – Disabled;	W / R	_
		-	1	Stand Alone – Enabled;	W / R	-
		-	-	Register – Communication Protocol;	-	-
313	0 to 1	_	0	Enables Communication Protocol DNP 3;	R	-
		_	1	Enables Communication Protocol – MODBUS ADDRESS;	R	_
314	0 to 254	-	-	Serial Communication Network Equipment Address;	R	1:1
		-	-	Register – Serial Communication Speed (BaudRate);	-	-
		-	0	Automatically Detects Communication Speed;	R	-
		-	1	Sets the communication speed at 2.400 bps;	R	-
315	0 to 6	-	2	Sets the communication speed at 4.800 bps;	R	-
		_	3	Sets the communication speed at 9.600 bps;	W / R - W / R - W / R - W / R - R - R - R - R 1:1 - - R - R - R - R - R - R - R -	
		_	4	Sets the communication speed at 19.200 bps;	R	_
		_	5	Sets the communication speed at 38.400 bps;	R	_
		_	6	Sets the communication speed at 57.600bps;	R	_
		-	-	Register – Communication Parity;	-	-
316	0 to 2	-	0	No Parity;	R	-
010		_	1	Even Parity;	R	_
		_	2	Odd Parity;	R	_
		-	-	Register – Write Protection;	W / R	-
317	0 to 1	-	0	Disables Write Protection via RS485;	W / R	-
		_	1	Enables Write Protection via RS485;	W / R	-
319	0 to 180	-	-	Data Log Acquisition Time – Minutes;	W / R	1:1
320	0 to 59	-	-	Seconds;	R	_

Electron

SERIAL COMMUNICATION

Bits Write Reading Range **Description / Point Name** MODBUS address State Index Read W/R 321 0 to 59 Minutes; _ W/R 322 0 to 23 Hours; _ 323 1 to 7 Week day; 1 = Monday; R _ W/R 324 1 to 31 Month Day; _ W/R 325 1 to 12 Month; _ 326 2016 to 2099 Year: W/R _ Register – Digital Input Type; _ **Digital Input Without Function;** W/R 0 _ Digital input by Command; W/R 1 327 0 to 8 _ 2 Digital Input by Regulation Set; W/R _ Digital Input by regulation operation mode; W/R 3 _ Parallel Mode Digital Input; W/R 8 _ Password Reminder; 328 0 to 9999 _ Register – Analog Output Type; 0 to 1 mA Analog Output; W/R 0 _ 0 to 5 mA Analog Output; W/R 1 330 0 to 4 _ 2 0 to 10 mA Analog Output; W/R _ 3 0 to 20 mA Analog Output; W/R _ W/R 4 4 to 20 mA Analog Output; _ Register – Analog Output Mirroring 1; _ W/R 0 Analog Ouput 1 Disabled; _ 2 Electrical Current Phase A – Secondary; W/R _ 331 0 to 22 Electrical Current Phase B – Secundário W/R 3 _ 4 Electrical Current Phase C – Secondary; W/R _ 5 Voltage on Phase A – Secondary; W/R _

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SERIAL COMMUNICATION

Bits Write **Reading Range MODBUS address Description / Point Name** State Index Read W/R 6 Voltage on Phase B – Secondary; _ 7 Voltage on Phase C – Secondary; W/R _ 8 Power Factor Phase A. W/R _ Power Factor Phase B: W/R 9 _ 10 Power Factor Phase C; W/R _ Active Power Phase A: W/R 11 _ 12 Active Power Phase B; W/R _ W/R 13 Active Power Phase C: _ 331 0 to 22 14 W/R Reactive Power Phase A; _ 15 **Reactive Power Phase B:** W/R _ W/R 16 Reactive Power Phase C; _ W/R 17 Apparent Power A: _ W/R 18 Apparent Power B; _ W/R 19 Apparent Power C; _ Voltage Variation of Phase A – Secondary; W/R 20 _ Voltage Variation of Phase B – Secondary; 21 W/R _ 22 Voltage Variation of Phase C – Secondary; W/R _ 332 0 to 22 Register – Analog Output Mirroring 2: (Same as Analog Output 1) W/R Register – Analog Output Mirroring 3: (Same as Analog Output 1) 333 0 to 22 W/R _ Register – Analog Output Mirroring 4: (Same as Analog Output 1) W/R 334 0 to 22 _ 335 0 to 22 Register – Analog Output Mirroring 5: (Same as Analog Output 1) W/R _ 336 TAP Changer Execution Hours – Partial; W/R 0 to 23 _ W/R TAP Changer Execution Days – Partial; 337 0 to 365 _ 338 0 to 9999 TAP Changer Execution Years – Partial; W/R _ Electrical Current Phase A – Secondary [A]; W/R 340 0.0 to 10.0 _ Min Range Analog Output

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Electron

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MODBUS address	Reading Range	Bits Index	State	Description / Point Na	me	Write Read	Scale
341	0.0 to 10.0	_		Electrical Current Phase B – Secondary [A];	Min Range Analog Output	W / R	1:10
342	0.0 to 10.0	-		Electrical Current Phase C – Secondary [A];	Min Range Analog Output	W / R	1:10
343	0.0 to 280.0	-	-	Voltage on Phase A – Secondary [V];	Min Range Analog Output	W / R	1:10
344	0.0 to 280.0	-	-	Voltage on Phase B – Secondary [V];	Min Range Analog Output	W / R	1:10
345	0.0 to 280.0	-	-	Voltage on Phase C – Secondary [V];	Min Range Analog Output	W / R	1:10
346	0.0 to 999.9	-	-	Active Power Phase A [W];	Min Range Analog Output	W / R	1:10
347	0.0 to 999.9	-	-	Active Power Phase B [W];	Min Range Analog Output	W / R	1:10
348	0.0 to 999.9	-	-	Active Power Phase C [W];	Min Range Analog Output	W / R	1:10
349	0.0 to 999.9	-	-	Reactive Power A [Var];	Min Range Analog Output	W / R	1:10
350	0.0 to 999.9	-	-	Reactive Power B [Var];	Min Range Analog Output	W / R	1:10
351	0.0 to 999.9	-		Reactive Power C [Var];	Min Range Analog Output	W / R	1:10
352	0.0 to 999.9	_		Apparent Power Phase A [VA];	Min Range Analog Output	W / R	1:10
353	0.0 to 999.9	-	-	Apparent Power Phase B [VA];	Min Range Analog Output	W / R	1:10
354	0.0 to 999.9	-	-	Apparent Power Phase C [VA];	Min Range Analog Output	W / R	1:10
356	0 to 23	-	-	TAP Changer Execution Hours – Total;		W / R	1:1
357	0 to 365	-	-	TAP Changer Execution Days – Total;		W / R	1:1
358	0 to 9999	-	-	TAP Changer Execution Years – Total;		W / R	1:1
359	0.0 to 10.0	-	-	Electrical Current Phase A – Secondary [A];	Max Range Analog Output	W / R	1:10
360	0.0 to 10.0	-	-	Electrical Current Phase B – Secondary [A];	Max Range Analog Output	W / R	1:10
361	0.0 to 10.0	-	-	Electrical Current Phase C – Secondary [A];	Max Range Analog Output	W / R	1:10
362	0.0 to 280.0	-	-	Voltage on Phase A – Secundário {V};	Max Range Analog Output	W / R	1:10
363	0.0 to 280.0	-	_	Voltage on Phase B – Secundário {V};	Max Range Analog Output	W / R	1:10
364	0.0 to 280.0	-	-	Voltage on Phase C – Secundário {V};	Max Range Analog Output	W / R	1:10
365	0.0 to 999.9	-	-	Active Power Phase A [W};	Max Range Analog Output	W / R	1:10
366	0.0 to 999.9	-	-	Active Power Phase B [W};	Max Range Analog Output	W / R	1:10



SERIAL COMMUNICATION

Bits Write **Reading Range MODBUS** address **Description / Point Name** State Scale Index Read Active Power Phase B [W]; 0.0 to 999.9 W/R 1:10 367 Max Range Analog Output _ W/R 368 Max Range Analog Output 0.0 to 999.9 Active Power Phase C [W]; 1:10 _ 369 0.0 to 999.9 Reactive Power Phase [A]; Max Range Analog Output W/R 1:10 _ Max Range Analog Output W/R 370 0.0 to 999.9 Reactive Power Phase [B] 1:10 _ 371 0.0 to 999.9 W/R 1:10 Reactive Power Phase [C]; Max Range Analog Output _ Max Range Analog Output W/R 372 0.0 to 999.9 Apparent Power Phase A [VA]; 1:10 _ 373 0.0 to 999.9 Apparent Power Phase B [VA]; Max Range Analog Output W/R 1:10 _ Apparent Power Phase C [VA]; W/R 0.0 to 999.9 374 Max Range Analog Output 1:10 _ Register – Display of quantities on Display Line OLED; _ _ 379 0 to 1 0 W/R Display on Fix Mode; _ _ W/R _ 1 Display on Scan mode; _ Register – Quantity Presentation on Display Line 1; 380 0 to 50 W/R _ _ Register – Quantity Presentation on Display Line 2; 381 0 to 50 W/R _ _ W/R 382 0 to 50 Register – Quantity Presentation on Display Line 3; 383 0 to 50 Register – Quantity Presentation on Display Line 4; W/R _ _ Register – Quantity Presentation on Display Line 5; 384 0 to 50 W/R _ _ Register – Enable/Disable Quantity on display, Line 1; W/R _ Enable Presentation on Line 1 – TAP Position Indication; W/R 0 1 _ 1 Enable Presentation on Line 1 – Secondary Voltage Phase A; W/R 1 _ 2 Enable Presentation on Line 1 – Primary Voltage on Phase A; W/R 1 _ 385 0 to 255 .3 1 Enable Presentation on Line 1 – Secondary Voltage on Phase B; W/R _ Enable Presentation on Line 1 – Primary Voltage on Phase B; W/R 4 1 5 Enable Presentation on Line 1 – Secondary Voltage on Phase C W/R 1 _ 6 Enable Presentation on Line 1 – Primary Voltage on Phase C; W/R 1 _ 7 W/R 1 Enable Presentation on Line 1 – Primary Apparent Power on Phase A; _

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		-	_	Register – Enable/Disable Quantity on display – Line 1;	-	-
		0	1	Enable Presentation on Line 1 – Current on Primary Phase A;	W / R	_
		1	1	Enable Presentation on Line 1 – Current on Secondary Phase B;	W / R	_
		2	1	Enable Presentation on Line 1 – Current on Primary Phase B;	W / R	_
386	0 to 255	3	1	Enable Presentation on Line 1 – Current on Secondary C;	W / R	_
		4	1	Enable Presentation on Line 1 – Current on Secondary C;	W / R	_
		5	1	Enable Presentation on Line 1 – Apparent Power on Primary Phase A;	W / R	_
		6	1	Enable Presentation on Line 1 – Apparent Power on Secondary A;	W / R	_
		7	1	Enable Presentation on Line 1 – Apparent Power on Primary Phase B;	W / R	_
		-	_	Register – Enable/Disable Quantity on display – Line 1;	-	-
		0	1	Enable Presentation on Line 1 – Apparent Power on Secondary Phase B;	W / R	_
		1	1	Enable Presentation on Line 1 – Apparent Power on Primary Phase C;	W / R	_
		2	1	Enable Presentation on Line 1 – Apparent Power on Secondary Phase A;	W / R	_
387	0 to 255	3	1	Enable Presentation on Line 1 – Active Power on Primary Phase A;	W / R	- - - - - - - - - - - -
		4	1	Enable Presentation on Line 1 – Active Power on Secondary Phase A;	W / R	_
		5	1	Enable Presentation on Line 1 – Active Power on Primary Phase B;	W / R	_
		6	1	Enable Presentation on Line 1 – Active Power on Secondary Phase B;	W / R	_
		7	1	Enable Presentation on Line 1 – Active Power on Primary Phase C;	W / R	_
		-	_	Register – Enable/Disable Quantity on display – Line 1;	-	-
		0	1	Enable Presentation on Line 1 – Active Power on Secondary Phase C;	W / R	-
	01.055	1	1	Enable Presentation on Line 1 – Reactive Power on Primary Phase A;	W / R	_
388	0 to 255	2	1	Enable Presentation on Line 1 – Reactive Power on Primary Phase B;	W / R	_
		3	1	Enable Presentation on Line 1 – Reactive Power on Secondary Phase B;	W / R	_
		4	1	Enable Presentation on Line 1 – Reactive Power on Primary Phase C;	W / R	-
		5	1	Enable Presentation on Line 1 – Reactive Power on Secondary Phase C;	W / R	_

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale	
388	0 to 255	6	1	Enable Presentation on Line 1 – Active Power on Secondary Phase B;	W / R	_	
200	010255	7	1	Enable Presentation on Line 1 – Active Power on Primary Phase C;	W / R	_	
		-	_	Register – Enable/Disable Quantity on display – Line 1;	-	-	
		0	1	Enable Presentation on Line 1 – Power Factor Phase B;	W / R	-	
		1	1	Enable Presentation on Line 1 – Power Factor Phase C;	W / R	-	
389	0 to 255	2	1	Enable Presentation on Line 1 – Compensated Voltage Phase A;	W / R	-	
303	010255	3	1	Enable Presentation on Line 1 – Compensated Voltage Phase B;	W / R	Read Scale W / R – R – R – R – R – – –	
		4	1	Enable Presentation on Line 1 – Compensated Voltage Phase C;	W / R	-	
		5	1	Enable Presentation on Line 1 – Voltage Deviation Phase A;	W / R	_	
		6	1	Enable Presentation on Line 1 – Voltage Deviation Phase B;	W / R	_	
		7	1	Enable Presentation on Line 1 – Voltage Deviation Phase C;	W / R	_	
		-	-	Register – Enable/Disable Quantity on display – Line 1;	-	_	
		0	1	Enable Presentation on Line 1 – Compensated Voltage Deviation Phase A;	W / R	_	
		1	1	Enable Presentation on Line 1 – Compensated Voltage Deviation Phase B;	W / R	-	
390	0 to 255	2	1	Enable Presentation on Line 1 – Compensated Voltage Deviation Phase C;	W / R	-	
390	010255	3	1	Enable Presentation on Line 1 – Load Percentage Phase A;	W / R	-	
		4	1	Enable Presentation on Line 1 – Load Percentage Phase B;	W / R	Read Image: Constraint of the second secon	
		5	1	Enable Presentation on Line 1 – Load Percentage Phase C;	W / R	_	
		6	1	Enable Presentation on Line 1 – Reference Voltage;	W / R	-	
		7	1	Enable Presentation on Line 1 – Line Frequency;	W / R	-	
430	_	-	-	Register – Relay Actuation Function;	R	_	
450		-	1	Relay with Function Command;	-	-	
		-	_	Register – Relay 1 Drive Logic;	-	-	
431	0 or 1	-	0	Normal Logic;	R	_	
		_	1	Inverse Logic;	R	_	

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SERIAL COMMUNICATION

MODBUS address **Reading Range Bits Index Description / Point Name** Write/Read State Scale Register – Relay Drive Type; _ 432 0 to 1 0 Pulse; R _ _ Constant; 1 R _ _ 433 0 to 5000 Relay Activation Time – miliseconds; R 1:1 _ Register – Relay 1 Drive by Command; R 434 _ Raise Voltage Command; 1 R _ _ Register – Relay with Function Command 2; R _ 440 _ Relay with Function Command; 1 R _ _ Register – Relay Actuation Type _ 441 0 or 1 0 Normal Logic; R _ _ 1 Inverse Logic; R _ _ Register – Relay Actuation Type; _ _ 442 0 or 1 Pulse; R 0 _ _ 1 Constant; R _ _ Relay Actuation Time 2 – Miliseconds; 443 0 to 5000 R _ _ Register – Relay Actuation by Command; _ _ 444 _ Lower Voltage Command; 0 1 R _ Register – Relay Actuation Function 3; _ _ Relay without function; 0 R _ _ 450 1 Relay with Function Command; R _ _ 2 Relay with Alarm Function; R _ _ 3 Relay with Failure Function; R _ _ Register – Relay Actuation Type 3; _ 451 0 or 1 Normal Logic; 0 R _ _ 1 Inverse Logic; R _ _ Register – Relay Actuation Type 3; _ _ 452 0 or 1 Pulse; 0 R _ _ R 1 Constant: _ _ 453 0 – 2 Relay Actuation Time 3 – Miliseconds; _ _ 26

Electron

IODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write/Read	Scale
		-		Register – Relay 3 Actuation by Command:		
		0	0	Lower Voltage OFF;	W / R	
	0 - 1	0	1	Lower Voltage ON;	W / R	
		1	0	Raise Voltage OFF;	W / R	
		1	1	Raise Voltage ON;	W / R	
		-		Register – Acionamento do Relé 3 por Alarmes;	-	
		0	0	Undervoltage OFF;	W / R	W / R - W / R
		0	1	Undervoltage ON;	W / R	-
		1	0	Overvoltage OFF;	W / R	W / R W / R W / R - W / R
			1	Overvoltage ON;	W / R	-
		2	0	Overcurrent OFF;	W / R	-
	0 - 63	2	1	Overcurrent ON;	W / R	_
45.4		3	0	Compensation Limit OFF;	W / R	-
454		5	1	Compensation Limit ON;	W / R	-
		4	0	Reverse Current OFF;	W / R	R
		4	1	Reverse Current ON;	W / R	
		5	0	TAP Changer Maintenance OFF;	W / R	
		5	1	TAP Changer Maintenance ON;	W / R	-
		-		Register – Acionamento do Relé 3 por Falha;	W / R	R
		0	0	Compensation Limit OFF;	W / R	
		0	1	Compensation Limit ON;	W / R	-
		1	0	Reverse Current OFF;	W / R	-
		1	1	Reverse Current ON;	W / R	
	0-31	2	0	TAP Changer Failure OFF;	W / R	-
			1	TAP Changer Failure ON;	W / R	
		2	0	TAP Changer Maintenance OFF;	W / R	
		3	1	TAP Changer Maintenance ON;	W / R	
		4	0	Regulation Failure OFF;	W / R	-
		4	1	Regulation Failure ON;		-

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		-	-	Register – Relay Actuation Function 4;	-	-
460		-	0	Relay without Function;	W/R	_
	0 to 3	-	1	Relay with Function Command;	W/R	-
		-	2	Relay with Alarm Function;	W/R	-
		-	3	Relay with Failure Function;	W/R	-
				Register – Relay 4 Actuation Logic;	_	-
461	0 or 1	-	0	Normal Logic;	W/R	-
		-	1	Inverse Logic;	W/R	-
		-	-	Register – Relay Actuation Type 4;		-
100		0	0	Lower Voltage OFF;	W / R	-
462	0 to 2	0	1	Lower Voltage ON;	W / R	_
		1	0	Raise Voltage OFF;	W/R	-
		-	1	Raise Voltage ON;	W/R	-
463	0 to 5000	-	-	Relay 4 Actuation Time – miliseconds;		-
		-	-	Register – Relay 4 actuation by Alarm;	-	-
		0	0	Undervoltage OFF:	W/R	-
			1	Undervoltage ON;	W/R	-
		1	0	Overvoltage OFF;	W/R	-
			1	Overvoltage ON;	W/R	-
464	0 to 63	2	0	Overcurrent OFF	W / R	_
			1	Overcurrent ON;	W / R	-
		3	0	Compensation Limit OFF;	W / R	-
			1	Compensation Limit ON;	W / R	-
		4	0	Reverse Current OFF;	W / R	-
		•	1	Reverse Current ON;	W / R	-

Electron

SERIAL COMMUNICATION	AL COMMUNICATION
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MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		-	_	Register – Relay 4 Actuation by Command;	-	_
		0	0	Lower Voltage OFF;	W / R	_
	0 to 1	0	1	Lower Voltage ON;	W / R	_
		1	0	Raise Voltage OFF;	W / R	_
	-	1	Raise Voltage ON;	W / R	_	
		-	-	Register – Relay 4 Actuation by Failure;		-
464		0	0	Compensation Limit OFF;	W / R	_
		0	1	Compensation Limit ON;	W / R	-
		1	0	Reverse Current OFF;	W / R	_
		-	1	Reverse Current ON;	W / R	_
	0 to 31	2	0	TAP Changer Failure OFF;	W / R	Read Scale W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - R - R - R - R - R - R - R - R - R - R - R -
		2	1	TAP Changer Failure ON;	W / R	_
		3	0	TAP Changer Maintenance OFF;	W / R	_
		5	1	TAP Changer Maintenance ON;	W / R	_
		4	0	Regulation Failure OFF;	W / R	_
		4	1	Regulation Failure ON;	W / R	_
		-	_	Register – Relay Actuation Function 5;	-	-
		-	0	Relay without Function;	R	Read Scale W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – W / R – R – R – R – R – R – R – R – R – R – R – R – R – R – R – R – R – R – – –
470	0-3	-	1	Relay with Function Command;	R	
		-	2	Relay with Alarm Function;	R	_
		-	3	Relay with Failure Function;	R	_
		-	-	Register – Relay 5 Actuation Logic;		-
471	0 to 1	-	0	Normal Logic;	R	_
		_	1	Inverse Logic;	R	_

Electron

SERIAL COMMUNICATION

Write Bits MODBUS address **Reading Range Description / Point Name** Scale State Index Read Register – Relay 4 Actuation by Command. _ _ 472 0 to 1 Pulse; 0 R _ _ 1 Constant; R _ _ 0 to 5000 Relay 5 Actuation Time – Miliseconds; 473 R 1:1 _ W/R Register – Relay 5 Actuation by Command; _ _ Lower Voltage OFF; W/R 0 _ 0 0 to 2 Lower Voltage ON; W/R 1 _ Raise Voltage OFF; W/R 0 _ 1 1 Raise Voltage ON; W/R _ Register – Relay 5 Actuation by Alarm; _ Undervoltage OFF; W/R 0 _ 0 Undervoltage ON; W/R 1 _ 0 Overvoltage OFF; W/R _ 1 Overvoltage ON; W/R 1 _ Overcurrent OFF; W/R 0 _ 474 2 1 Overcurrent ON; W/R — 0 - 630 Compensation Limit OFF; W/R _ 3 W/R 1 Compensation Limit ON; _ Reverse Current OFF; W/R 0 _ 4 Reverse Current ON; W/R 1 _ TAP Changer Maintenance OFF; 0 W/R _ 5 TAP Changer Maintenance ON; W/R 1 _ Register – Relay 5 Actuation by Failure; _ _ 0 - 31Compensation Limit OFF; W/R 0 _ 0 Compensation Limit ON; W/R 1 _

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		1	0	Reverse Current OFF;	W / R	_
		T	1	Reverse Current ON;	W / R	-
		2	0	TAP Changer Failure OFF;	W / R	_
474	0 to 31	2	1	TAP Changer Failure ON;	W/R	_
0 10 31	3	0	TAP Changer Maintenance OFF;	W/R	_	
	,	1	TAP Changer Maintenance ON;	W/R	_	
		4	0	Regulation Failure OFF;	W/R	_
		4	1	Regulation Failure ON;	W/R	_
		-	-	Register – Relay Actuation Function 6;	-	-
		I	0	Relay without Function;	R	_
480	0 – 3	I	1	Relay with Function Command;	R	_
		I	2	Relay with Alarm Function;	R	-
		I	3	Relay with Failure Function;	R	_
		-	-	Register – Relay 6 Actuation Logic;	-	-
481	0 to 1	Ι	0	Normal Logic;	W/R – W/R – W/R – W/R – R – R – R – R –	_
		Ι	1	Inverse Logic;	R	_
		-	-	Register – Relay 6 Actuation Type ;	-	-
482	0 – 2		0	Pulse;	R	-
			1	Constant;	R	_
483	0 to 500	-	_	Relay 6 Actuation Time- miliseconds;	R	1:1

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale		
		_		Register – Relay 6 Actuation time by Command;	-			
		0	0	Lower Voltage OFF;	W / R	_		
		0	1	Lower Voltage ON;	W / R	-		
		1	0	Raise Voltage OFF;	W / R	-		
		1	1	Raise Voltage ON;	W / R	-		
		-	_	Register – Relay 6 Actuation by Alarm;	W / R	-		
		0	0	Undervoltage OFF;	W / R	-		
		0	1	Undervoltage ON;	W / R	-		
484	0 - 63	1	0	Overvoltage OFF;	W / R	-		
		-	1	Overvoltage ON;	W / R	_		
		2	0	Overcurrent OFF;	W / R	_		
			1	Overcurrent ON;	W / R	_		
		3	0	Compensation Limit OFF;	W / R	_		
			1	Compensation Limit ON;	W / R	_		
		4	0 Reverse Current OFF;	W / R	_			
		-	1	Reverse Current ON;	W / R	_		
		5	0	Regulation Failure OFF;	W / R	W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R - W / R -		
		5	1	Regulation Failure ON;	W / R	_		
		-	-	Register – Relay 6 Actuation by Failure;	-	-		
		0	0	Compensation Limit OFF;	W / R	_		
		Ŭ	1	Compensation Limit ON;	-	-		
484	0-31	1	0	Reverse Current OFF;		-		
		-	1	Reverse Current ON;	-	_		
		2	0	TAP Changer Failure OFF;		-		
		2	1	TAP Changer Failure ON;	W / R	_		

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale	
		2	0	TAP Changer Maintenance OFF;	W/R	_	
484	0-31	3	1	TAP Changer Maintenance ON;	W / R	_	
404	0-51		0	Regulation Failure OFF;	W / R	_	
		4	1	Regulation Failure ON;	W/R	_	
		_		Register – Relay 7 Actuation Function;	-	_	
		_	0	Relay without Function;	W / R	_	
490	0 a 3	_	1	Relay with Function Command;	W / R	_	
	_	2	Relay with Alarm Function;	W / R	-		
	_	3	Relay with Failure Function;	W / R	_		
		_		Register – Relay 7 Actuation Logic	-	_	
491 –	-	-	_	0	Normal Logic;	R	_
		_	1	Reverse Logic;	R	_	
492 0 to 1		-		Register – Relay Actuation Type 7;	-	_	
	0 to 1	_	0	Normal;	W / R	_	
	-	_	1	Inverse;	W / R	_	
493	0 to 5000	-		Relay 7 Actuation Time – miliseconds;	R	1:1	
		-		Register – Relay 7 Actuation by Command;	-	-	
		0	0	Lower Voltage OFF;	W / R	-	
	0 – 2	0	1	Lower Voltage ON;	W / R	_	
		1	0	Raise Voltage OFF;	W / R	_	
		Ŧ	1	Raise Voltage ON;	W / R	_	
494		_		Register – Relay 7 Actuation by Alarm;	-	_	
		0	0	Undervoltage OFF;	W / R	-	
			1	Undervoltage ON;	W / R	—	
	0 - 63	1	0	Overvoltage OFF;	W / R	—	
			1	Overvoltage ON;	W / R	_	
		2	0	Overcurrent OFF;	W / R	-	
		۷	1	Overcurrent ON;	W / R	_	

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		3	0	Compensation Limit OFF	W / R	_
		5	1	Compensation Limit ON	W/R	-
	0 - 63	4	0	Reverse Current OFF;	W/R	-
		4	1	Reverse Current ON;	W/R	-
		5	0	TAP Changer Maintenance OFF;	W/R	-
		5	1	TAP Changer Maintenance ON;	W/R	_
		-	-	Register – Relay 7 actuation by Failure;		-
		0	0	Compensation Limit OFF;	W/R	_
495		0	1	Compensation Limit ON;	W/R	_
		1	0	Reverse Current OFF;	W/R	_
		-	1	Reverse Current ON;	W/R	_
	0-31	2	0	TAP Changer Failure OFF;	W/R	-
			1	TAP Changer Failure ON;	W/R	-
		3	0	TAP Changer Maintenance OFF;	W/R	_
			1	TAP Changer Maintenance ON;	W/R	_
		4	0	Regulation Failure OFF;	W/R	_
			1	Regulation Failure ON;	W/R	_
			-	Register – Relay Actuation Function;	-	•
		_	0	Relay without Function;	R	-
500	0 - 3	_	1	Relay with Function Command;	R	-
		-	2	Relay with Alarm Function;	R	-
		-	3	Relay with Failure Function;	R	-

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
501	0 to 1	-		Register – Relay 8 Actuation Logic;		-
		-	0	Normal Logic;	R	_
		_	1	Inverse Logic;	R	_
502	0 to 1	_		Register – Relay 8 Actuation by Command;		-
		-	0	Pulse;	R	_
		-	1	Constant;	R	_
503	0 to 5000	-	_	Relay 8 Actuation Time – Miliseconds;	R	1:1
504	0 – 2	-		Register – Relay 8 Actuation by Command	_	
		0	0	Lower Voltage OFF;	W/R	-
			1	Lower Voltage ON;	W/R	-
		1	0	Raise Voltage OFF;	W/R	_
			1	Raise Voltage ON;	W/R	_
	0 – 63	-		Register – Relay 8 Actuation by Alarm;		-
		0	0	Undervoltage OFF;	W/R	-
			1	Undervoltage ON;	W / R	_
		1	0	Overvoltage OFF;	W / R	_
			1	Overvoltage ON;	W / R	_
		2	0	Overcurrent OFF;	W / R	_
			1	Overcurrent ON;	W/R	-
		3	0	Compensation Limit OFF;	W/R	-
			1	Compensation Limit ON;	W/R	-
		4	0	Reverse Current OFF;	W/R	_
			1	Reverse Current ON;	W/R	_
		5	0	TAP Changer Maintenance OFF;	W/R	_
			1	TAP Changer Maintenance ON;	W/R	_

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
504	0 to 31	-	_	Register – Relay 8 Actuation by Failure;	-	-
		0	0	Compensation Limit OFF;	W / R	_
			1	Compensation Limit ON;	W / R	_
		1	0	Reverse Current OFF;	W / R	_
			1	Reverse Current ON;	W / R	_
		2	0	TAP Changer Failure OFF;	W / R	_
			1	TAP Changer Failure ON;	W / R	_
		3	0	TAP Changer Maintenance OFF;	W / R	_
			1	TAP Changer Maintenance ON;	W / R	_
		4	0	Regulation Failure OFF;	W / R	_
			1	Regulation Failure ON;	W / R	_
510	0 – 3	_		Register – Relay Actuation Function 9;		
		_	0	Relay without Function;	R	_
		_	1	Relay with Function Command;	R	_
		_	2	Relay with Alarm Function;	R	_
		_	3	Relay with Failure Function;	R	_
511	0 to 1	-	_	Register – Relay 9 Actuation Logic;	-	_
		_	0	Normal Logic;	R	_
		_	1	Inverse Logic;	R	_
512	0 to 1	_		Register – Relay 9 Actuation by Command;	_	
		-	0	Pulse;	R	_
		-	1	Constant;	R	_
513	0 to 5000	-	_	Relay 9 Actuation Time – Miliseconds;	R	1:1

Electron

SERIAL COMMUNICATION

Write Bits **MODBUS** address **Reading Range Description / Point Name** State Index Read Register – Relay 9 Actuation by Command; _ Lower Voltage OFF; W/R 0 0 0 - 21 Lower Voltage ON; W/R W/R Raise Voltage OFF; 0 1 W/R 1 Raise Voltage ON; Register – Relay 9 Actuation by Alarm; _ Undervoltage OFF; W/R 0 0 Undervoltage ON; W/R 1 Overvoltage OFF; 0 W/R 1 1 Overvoltage ON; W/R Overcurrent OFF; W/R 0 2 Overcurrent ON: W/R 1 0 - 63514 0 Compensation Limit OFF; W/R 3 Compensation Limit ON; W/R 1 Reverse Current OFF; W/R 0 4 Reverse Current ON; W/R 1 0 TAP Changer Maintenance OFF; W/R 5 TAP Changer Maintenance ON; W/R 1 Register – Relay 9 Actuation by Failure; _ Compensation Limit OFF; W/R 0 0 W/R 1 Compensation Limit ON; Reverse Current OFF; W/R 0 0 to 31 1 Reverse Current ON; W/R 1

TAP Changer Failure OFF;

TAP Changer Failure ON;

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Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale						
195		3	0	TAP Changer Maintenance OFF;	W / R	-						
195	0 to 31	5	1	TAP Changer Maintenance ON;	W / R	_						
196	01001	4	0	Regulation Failure OFF;	W / R	_						
190		4	1	Regulation Failure ON;	W / R	-						
		-	-	Register – Relay Actuation Function 10;	-	-						
		-	0	Relay without Function;	R	-						
520	0 - 3	-	1	Relay with Function Command;	R	_						
		-	2	Relay with Alarm Function;	R	_						
		-	3	Relay with Failure Function;	R	-						
		-	_	Register – Relay 10 Actuation Logic;	-	-						
521	0 to 1	I	0	Normal Logic;	R	_						
			I	1	Inverse Logic;	R	_					
	0 to 1	0 to 1	0 to 1	0 to 1	0 to 1	0 to 1	0 to 1	-	-	Register – Relay 10 Actuation by Command;	-	-
522								0 to 1	0 to 1	0 to 1	0 to 1	-
		-	1	Constant;	R	_						
523	0 to 5000	-	_	Relay 10 Actuation Time – Miliseconds;	R	1:1						
		-	-	Register – Relay 10 Actuation by Command;	-	-						
		0	0	Lower Voltage OFF;	W / R	_						
	0 - 2	0	1	Lower Voltage ON;	W / R	_						
		1	0	Raise Voltage OFF;	W / R	_						
524			1	Raise Voltage ON;	W / R	_						
			-	Register – Relay 10 Actuation by Alarm;	-	-						
	0 - 3	0	0	Undervoltage OFF;	W / R	-						
				U	1	Undervoltage ON;	W / R	_				

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale	
		1	0	Overvoltage OFF;	W / R	_	
		1	1	Overvoltage ON;	W / R	_	
		2	0	Overcurrent OFF;	W / R	_	
	0 – 3	Z	1	Overcurrent ON;	W / R	_	
		3	0	Compensation Limit OFF;	W / R	_	
		5	1	Compensation Limit ON;	W / R	_	
		4	0	Reverse Current OFF;	W / R	_	
		4	1	Reverse Current ON;	W / R	_	
		5	0	TAP Changer Maintenance OFF;	W / R	_	
		5	1	TAP Changer Maintenance ON;	W / R	_	
524		_		Register – Relay 10 Actuation by Failure;			
		0	0	Compensation Limit OFF;	W / R	_	
		0	1	Compensation Limit ON;	W / R	-	
		1	0	Reverse Current OFF;	W / R	-	
		1	1	Reverse Current ON;	W / R	-	
	0 to 31	2	0	TAP Changer Failure OFF;	W / R	-	
		Z	1	TAP Changer Failure ON;	W / R	-	
		3	0	TAP Changer Maintenance OFF;			
		J	1	TAP Changer Maintenance ON;			
		4	0	Regulation Failure OFF;			
		4	1	Regulation Failure ON;			
		_		Register – Relay Actuation Function 11;	-		
		_	0	Relay without Function;	R	-	
530	0 – 3	_	1	Relay with Function Command;	R	-	
		_	2	Relay with Alarm Function;	R	-	
		_	3	Relay with Failure Function;	R	_	

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		-		Register – Relay 11 Actuation logic;	—	
531	0 to 1	_	0	Normal Logic;	R	_
		_	1	Inverse Logic;	R	_
		-	-	Register – Relay 11 Actuation by Command;	-	-
532	0 to 1	-	0	Pulse;	R	_
	-	1	Constant;	R	-	
533	0 to 5000	-	_	Relay 11 Actuation Time – Miliseconds;	R	1:1
		-	_	Register – Relay 10 Actuation by Command;	-	-
	0 – 2	0 0	0	Lower Voltage OFF;	W / R	-
		0	1	Lower Voltage ON;	W / R	-
		1	0	Raise Voltage OFF;	W / R	-
		1	1	Raise Voltage ON;	W / R	-
		-	_	Register – Relay 11 Actuation by Alarm;	-	-
		0	0	Undervoltage OFF;	W / R	-
		0	1	Undervoltage ON;	W / R	-
		1	0	Overvoltage OFF;	W / R	-
		1	1	Overvoltage ON;	W / R	-
		2	0	Overcurrent OFF;	W / R	-
534	0 - 63	2	1	Overcurrent ON;	W / R	-
		3	0	Compensation Limit OFF;	W / R	-
		5	1	Compensation Limit ON;	W / R	-
		4	0	Reverse Current OFF;	W / R	_
		4	1	Reverse Current ON;	W / R	-
		5	0	TAP Changer Maintenance OFF;	W / R	_
		5	1	TAP Changer Maintenance ON;	W / R	_
	0-31	-	_	Register – Relay 11 Actuation by Failure;	-	-
0-:	0-31	0	0	Compensation Limit OFF;	W/R	-

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write / Read	Scale						
			1	Compensation Limit ON;	W / R	_						
		1	0	Reverse Current OFF;	W / R	_						
		Ŧ	1	Reverse Current ON;	W / R	_						
534	0-31	2	0	TAP Changer Failure OFF;	W / R	-						
554		2	1	TAP Changer Failure ON;	W / R	-						
		3	0	Reverse Current OFF;	W / R	-						
			5	1	Reverse Current ON;	W / R	-					
		4	0	TAP Changer Maintenance OFF;	W / R	-						
		+	1	TAP Changer Maintenance ON;	W / R	-						
		-	-	Register – Relay Actuation Function 12;	_							
			0	Relay without Function;	W / R	-						
540	0-3		1	Relay with Function Command;	W / R	-						
		_	2	Relay with Alarm Function;	W / R	_						
		-	3	Relay with Failure Function;	W / R	-						
	0 or 1	0 or 1	0 or 1	0 or 1	0 or 1	0 or 1	0 or 1	-	-	Register – Relay 12 Actuation Logic;	_	
541								0 or 1	0 or 1	_	0	Normal Logic;
		_	1	Inverse Logic;	W / R	_						
		-	_	Register – Relay 12 Actuation by Command;	_							
542	0 to 1	0 to 1	0 to 1	0 to 1	0 to 1	_	0	Pulse;	W / R	_		
		_	1	Constant;	W / R	_						
543	0 to 5000	-	_	Relay 12 Actuation Time – Miliseconds;	W / R	1:1						
		-	-	Register – Relay 10 Actuation by Command;	_							
		0	0	Lower Voltage OFF;	W / R	_						
544	0-2	0	1	Lower Voltage ON;	W / R	_						
544	0-2	1	0	Raise Voltage OFF;	W / R	_						
			1	Raise Voltage ON;	W / R	_						
		-	-	Register – Relay 12 Actuation by Alarm;	_							

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		0	0	Undervoltage OFF;	W / R	_
		0	1	Undervoltage ON;	W / R	_
		1	0	Overvoltage OFF;	W/R	_
		1	1	Overvoltage ON;	W / R	-
		2	0	Overcurrent OFF;	W / R	-
	0 (2	2	1	Overcurrent ON;	W / R	-
	0 – 63	3	0	Compensation Limit OFF;	W / R	-
		5	1	Compensation Limit ON;	W / R	-
		4	0	Reverse Current OFF;	W / R	_
			1	Reverse Current ON;	W / R	-
		5	0	TAP Changer Maintenance OFF;	W / R	-
			1	TAP Changer Maintenance ON;	W / R	-
544		-		Register – Relay 12 Actuation by Failulre;		-
		0	0	Compensation Limit OFF;	W / R	-
		0	1	Compensation Limit ON;	W / R	-
		1	0	Reverse Current OFF;	W / R	-
		Ţ	1	Reverse Current ON;	W / R	_
	0 to 31	2	0	TAP Changer Failure OFF;	W / R	_
		2	1	TAP Changer Failure ON;	W / R	-
		3	0	Reverse Current OFF;	W / R	-
		5	1	Reverse Current ON;	W / R	-
		4	0	TAP Changer Maintenance OFF;	W / R	-
		4	1	TAP Changer Maintenance ON;	W/R	_

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		-	_	Register – Relay Actuation Function 13;	-	-
		_	0	Relay without Function;	R	_
550	0-3	-	1	Relay with Function Command;	R	-
		-	2	Relay with Alarm Function;	R	_
		-	3	Relay with Failure Function;	R	-
		-	_	Register – Relay 13 Actuation Logic;	-	-
551	0 to 1	-	0	Normal Logic;	R	_
		-	1	Inverse Logic;	R	-
		-	_	Register – Relay 13 Actuation by Command;	_	-
552	0 or 1	_	0	Pulse;	R	_
		_	1	Constant;	R	_
553	0 to 5000	-	_	Relay 13 Actuation Time – Miliseconds;	R	1:1
		_		Register – Relay 13 Actuation by Command;	_	-
			0	0	Lower Voltage OFF;	W / R
	0 - 2	0	1	Lower Voltage ON;	W / R	_
		1	0	Raise Voltage OFF;	W / R	_
		-	1	Raise Voltage ON;	W / R	_
		-	-	Register – Relay 13 Actuation by Alarm;	-	-
554		0	0	Undervoltage OFF;	W / R	_
		0	1	Undervoltage ON;	W / R	_
		1	0	Overvoltage OFF;	W / R	_
	0 - 63	-	1	Overvoltage ON;	W / R	_
		2	0	Overcurrent OFF;	W / R	_
			1	Overcurrent ON;	W / R	-
		3	0	Compensation Limit OFF;	W/R	-

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale		
		3	1	Compensation Limit ON;	W / R	_		
	0 - 63	4	0	Reverse Current OFF;	W/R	_		
		4	1	Reverse Current ON;	W/R	-		
		5	0	TAP Changer Maintenance OFF;	W/R	-		
		5	1	TAP Changer Maintenance ON;	W/R	-		
		-	-	Register – Relay 13 Actuation by Failure;	-	-		
	554	0	0	Compensation Limit OFF;	W/R	-		
554		0	1	Compensation Limit ON;	W/R	-		
554		1	0	Reverse Current OFF;	W/R	-		
		1	1	Reverse Current ON;	W/R	-		
	0 to 31	2	0	TAP Changer Failure OFF;	W/R	-		
			1	TAP Changer Failure ON;	W/R	-		
		3	0	TAP Changer Maintenance OFF;	W/R	-		
		5	1	TAP Changer Maintenance ON;	W/R	-		
		4	0	Regulation Failure OFF;	W/R	-		
		4	1	Regulation Failure ON;	W/R	-		
560	0-3	-	-	Register – Relay Actuation Function 14;	-	-		
200	0-5	-	3	Failures;	R	_		
		-	_	Register – Relay 14 Actuation Logic;	_	-		
561	0 or 1	_	0	Normal Logic;	W / R	_		
		F	F	_	1	Inverse Logic;	W/R	_

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		_		Register – Relay 14 Actuation by Failure;	-	- -
		0	0	Compensation Limit OFF;	W / R	-
		0	1	Compensation Limit ON;	W / R	-
		1	0	Reverse Current OFF;	W / R	-
		1	1	Reverse Current ON;	W / R	-
564	0 to 31	2	0	TAP Changer Failure OFF;	W / R	-
		2	1	TAP Changer Failure ON;	W / R	-
		3	0	TAP Changer Maintenance OFF;	W / R	-
		5	1	TAP Changer Maintenance ON;	W / R	-
		4	0	Regulation Failure OFF;	W / R	-
		4	1	Regulation Failure ON;	W / R	-
		-	_	Register – Status and Command of Equipment 1 to Parallelism:		
		_	0	Equipment on Follower Mode;	W / R	-
		_	2	Equipment on individual Mode;	W / R	-
		_	4	Min TAP Reset;	W	-
570	-	_	5	Max TAP Reset;	W	-
		_	6	Raise TAP Command address 1;	W	-
		_	7	Lower TAP Command address 1;	W	-
		_	8	Failure/Synchronization on address 1;	W	_
		_	9	Equipament 1 Reset;	W	_
571	50 – 150	-	_	Current TAP Position on Address 1;	R	-100
572	50 - 150	-	_	Minimum TAP Position on Address 1;	R	-100
573	50 - 150	-	_	Maximum TAP Position on Address 1	R	-100

Electron

MODBUS address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
		-	-	Register – Relay 14 actuation by Failure;	-	-
		0	1	Crown Failure;	R	-
		1	1	TAP Changer Raise Failure;	R	-
574		2	1	TAP Changer lower Failure;	R	-
574		3	1	Synchronization Failure;	R	-
	4	1	Parallelism Communication Failure;	R	-	
	5	1	TAP Changer Min TAP Failure;	R	-	
		6	1	TAP Changer Max TAP Failure;	R	-
575	0 - 9	-	-	Register – Status and Command of Equipment 2 to Parallelism;	As Add	dr. 570
576	50 – 150	-	-	Current TAP Position on Address 2;	R	-100
577	50 – 150	-	-	Minimum TAP Position on Address 2;	R	-100
578	50 – 150	-	-	Maximum TAP Position on Address 2;	R	-100
579	-	-	-	Register – Address Failure 2;	As Ado	dr. 574
580	0-9	_	_	Register – Status and Command of Equipment 3 to Parallelism;	As Add	dr. 570
581	50 - 150	_	_	Current TAP Position on Address 3;	R	-100
582	50 - 150	_	_	Minimum TAP Position on Address 3;	R	-100
583	50 - 150	_	_	Maximum TAP Position on Address 3;	R	-100
584	-	-	-	Register – Address Failure 3;	As Add	dr. 574
585	0-9	_	_	Register – Status and Command of Equipment 4 to Parallelism;	As Add	dr. 570
586	50 - 150	_	_	Current TAP Position on Address 4;	R	- 100
587	50 - 150	_	_	Minimum TAP Position on Address 4.	R	- 100
588	50 - 150	_	_	Maximum TAP Position on Address 4;	R	- 100
589	-	_	_	Register – Address Failure 4;	As Add	dr. 574
590	0-9	_	_	Register – Status and Command of Equipment 5 to Parallelism;	As Add	dr. 570
591	50 - 150	_	_	Current TAP Position on Address 5;	R	-100

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Sca
592	0 – 9	_	_	Minimum TAP Position on Address 5;	R	-1
593	50 – 150	-	-	Maximum TAP Position on Address 5;	R	-1
594	-	-	-	Register – Address Failure 5;		
595	0 - 9	-	-	Register – Status and Command of Equipment 6 to Parallelism;	As Add	r. 570
596	50 – 150	-	-	Current TAP Position on Address 6;	R	- 1
597	50 – 150	-	-	Minimum TAP Position on Address 6;	R	- 1
598	50 – 150	-		Maximum TAP Position on Address 6;	R	- 1
599	-	-	-	Register – Address Failure 6;		
600	0 - 9	-	-	Register – Status and Command of Equipment 7 to Parallelism;	As Add	r. 570
601	50 – 150	-	-	Current TAP Position on Address	R	- 1
602	50 – 150	-	-	Minimum TAP Position on Address 7;	R	- 1
603	50 – 150	-	-	Maximum TAP Position on Address 7;	R	- 1
604	_	-	-	Register – Address Failure 7;		
605	0 – 9	-	-	Register – Status and Command of Equipment 8 to Parallelism;	As Add	r. 570
606	50 – 150	-	-	Current TAP Position on Address	R	- 1
607	50 – 150	-	-	Minimum TAP Position on Address 8;	R	- 1
608	50 – 150	-	-	Maximum TAP Position on Address 8;	R	- 1
609	_	-	-	Register – Address Failure 8;		
610	0 - 9	_	-	Register – Status and Command of Equipment 9 to Parallelism;	As Add	r. 570
611	50 – 150	_	-	Current TAP Position on Address	R	- 1
612	50 – 150	_	-	Minimum TAP Position on Address 9;	R	- 1
613	50 – 150	-	-	Maximum TAP Position on Address 9;	R	- 1
614	_	-	-	Register – Address Failure 9;		
615	0 - 9	-	-	Register – Status and Command of Equipment 10 to Parallelism;	As Add	r. 570
616	50 - 150	0-9	-	Current TAP Position on Address 10;	R	- 1

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Sca
617	50 - 150	_	_	Minimum TAP Position on Address 10;	R	- 1
618	50 – 150	-	-	Maximum TAP Position on Address 10;	R	- 1
619	-	-	-	Register – Address Failure 11;		
620	0 - 9	-	-	Register – Status and Command of Equipment 11 to Parallelism;	As Ad	dr. 570
621	50 – 150	-	-	Current TAP Position on Address 11;	R	- 10
622	50 – 150	-	-	Minimum TAP Position on Address 11;	R	- 1
623	50 – 150	-	-	Maximum TAP Position on Address 11;	R	- 1
624	-	-	-	Register – Address Failure 12;	As Ad	dr. 574
625	0 – 9	-	-	Register – Status and Command of Equipment 12 to Parallelism;	As Ad	dr. 570
626	50 – 150	-	-	Current TAP Position on Address 12;	R	- 1
627	50 – 150	-	-	Minimum TAP Position on Address 12;	R	- 1
628	50 – 150	-	-	Maximum TAP Position on Address 12;	R	- 1
629	_	-	-	Register – Address Failure 13;	As Ad	dr. 574
630	0 – 9	-	-	Register – Status and Command of Equipment 13 to Parallelism;	As Ad	dr. 570
631	50 – 150	-	-	Current TAP Position on Address 13;	R	- 1
632	50 – 150	-	-	Minimum TAP Position on Address 13;	R	- 1
633	50 – 150	-	-	Maximum TAP Position on Address 13;	R	- 1
634	_	-	_	Register – Address Failure 14;	As Ad	dr. 574
635	0 - 9	-	_	Register – Status and Command of Equipment 14 to Parallelism;	As Ad	dr. 570
636	50 - 150	_		Current TAP Position on Address 14;	R	- 1
637	50 – 150	-		Minimum TAP Position on Address 14;	R	- 1
638	50 – 150	-	-	Maximum TAP Position on Address 14;	R	- 1
639	_			Register – Address Failure 14;	As Ad	dr. 574
640	0 - 9			Register – Status and Command of Equipment 15 to Parallelism;	As Ad	dr. 570
641	50 - 150			Current TAP Position on Address 15;	R	- 10

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
642	50 – 150	-	-	Minimum TAP Position on Address 15;	R	- 100
643	50 – 150	_	-	Maximum TAP Position on Address 15;	R	- 100
644	-	_	-	Register – Address Failure 15;	As Ado	dr. 574
645	0 - 9	_	-	Register – Status and Command of Equipment 16 to Parallelism;	As Ado	dr. 570
646	50 – 150	_	-	Current TAP Position on Address 16;	R	- 100
647	50 – 150	_	-	Minimum TAP Position on Address 16;	R	- 100
648	50 - 150	_	_	Maximum TAP Position on Address 16;	R	- 100
649	-	_	_	Register – Address Failure 16;	As Ado	dr. 574
650	0-9	_	_	Register – Status and Command of Equipment 17 to Parallelism;	As Ado	dr. 570
651	50 - 150	_	_	Current TAP Position on Address 17;	R	- 100
652	50 - 150	_	_	Minimum TAP Position on Address 17;	R	- 100
653	50 - 150	_	_	Maximum TAP Position on Address 17;	R	- 100
654	-	_	_	Register – Address Failure 17;	As Ado	dr. 574
655	0-9	_	-	Register – Status and Command of Equipment 18 to Parallelism;	As Ado	dr. 570
656	50 - 150	_	-	Current TAP Position on Address 18;	R	- 100
657	50 - 150	_	_	Minimum TAP Position on Address 18;	R	- 100
658	50 - 150	_	_	Maximum TAP Position on Address 18;	R	- 100
659	-	_	_	Register – Address Failure 18;	As Ado	dr. 574
660	0-9	_	-	Register – Status and Command of Equipment 19 to Parallelism;	As Ado	dr. 570
661	50 - 150	_	_	Current TAP Position on Address 19;	R	- 100
662	50 - 150	_	_	Minimum TAP Position on Address 19;	R	- 100
663	50 - 150	_	-	Maximum TAP Position on Address 19;	R	- 100
664	_	_	-	Register – Address Failure 19;	As Ado	dr. 574
665	0-9	-	-	Register – Status and Command of Equipment 20 to Parallelism;	As Ado	dr. 570
666	50 - 150	_	_	Current TAP Position on Address 20;	R	- 100

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
667	50 – 150	_	-	Minimum TAP Position on Address 20;	R	- 100
668	50 – 150	-	-	Maximum TAP Position on Address 20;	R	- 100
669	-	_	-	Register – Address Failure 20;	As Ade	dr. 574
670	0 - 9	_	-	Register – Status and Command of Equipment 21 to Parallelism;	As Ade	dr. 570
671	50 – 150	_	-	Current TAP Position on Address 21;	R	- 100
672	50 – 150	_	-	Minimum TAP Position on Address 21;	R	- 100
673	50 – 150	-	-	Maximum TAP Position on Address 21;	R	- 100
674	-	_	_	Register – Address Failure 21;	As Ade	dr. 574
675	0-9	_	-	Register – Status and Command of Equipment 22 to Parallelism;	As Ade	dr. 570
676	50 - 150	_	_	Current TAP Position on Address 22;	R	- 100
677	50 - 150	_	_	Minimum TAP Position on Address 22;	R	- 100
678	50 – 150	_	-	Maximum TAP Position on Address 22;	R	- 100
679	-	_	-	Register – Address Failure 22;	As Ade	dr. 574
680	0-9	_	_	Register – Status and Command of Equipment 23 to Parallelism;	As Ade	dr. 570
681	50 - 150	_	_	Current TAP Position on Address 23;	R	- 100
682	50 - 150	_	_	Minimum TAP Position on Address 23;	R	- 100
683	50 - 150	_	_	Maximum TAP Position on Address 23;	R	- 100
684	-	_	-	Register – Address Failure 23;	As Ade	dr. 574
685	0-9	_	_	Register – Status and Command of Equipment 24 to Parallelism;	As Ade	dr. 570
686	50 – 150	_	_	Current TAP Position on Address 24;	R	- 100
687	50 – 150	_	_	Minimum TAP Position on Address 24;	R	- 100
688	50 - 150	_	-	Maximum TAP Position on Address 24;	R	- 100
689	-	_	-	Register – Address Failure 24;		-
690	0 - 9	_	-	Register – Status and Command of Equipment 25 to Parallelism;	As Ade	dr. 570
691	50 - 150	_	-	Current TAP Position on Address 25;	R	- 100

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
692	50 – 150	_	-	Minimum TAP Position on Address 25;	R	- 100
693	50 – 150	_	-	Maximum TAP Position on Address 25;	R	- 100
694	-	_	-	Register – Address Failure 25;	As Add	dr. 574
695	0 - 9	_	-	Register – Status and Command of Equipment 26 to Parallelism;	As Ado	dr. 570
696	50 – 150	_	-	Current TAP Position on Address 26;	R	- 100
697	50 – 150	_	-	Minimum TAP Position on Address 26;	R	- 100
698	50 – 150	_	-	Maximum TAP Position on Address 26;	R	- 100
699	-	-	-	Register – Address Failure 26;	As Ad	dr.574
700	0 - 9	-	-	Register – Status and Command of Equipment 27 to Parallelism;	As Add	dr. 570
701	50 – 150	-	-	Current TAP Position on Address 27;	R	- 100
702	50 – 150	-	-	Minimum TAP Position on Address 27;	R	- 100
703	50 – 150	-	-	Maximum TAP Position on Address 27;	R	- 100
704	-	-	-	Register – Address Failure 27;	As Ad	dr.574
705	0 – 9	_	-	Register – Status and Command of Equipment 28 to Parallelism;	As Ado	dr. 570
706	50 – 150	_	-	Current TAP Position on Address 28;	R	- 100
707	50 – 150	_	-	Minimum TAP Position on Address 28;	R	- 100
708	50 – 150	-	-	Maximum TAP Position on Address 28;	R	- 100
709	-	-	-	Register – Address Failure 28;	-	_
710	0 - 9	-	-	Register – Status and Command of Equipment 29 to Parallelism;	As Ado	dr. 570
711	50 - 150	-	-	Current TAP Position on Address 29;	R	- 100
712	50 - 150	-	-	Minimum TAP Position on Address 29;	R	- 100
713	50 – 150	-	-	Maximum TAP Position on Address 29;	R	- 100
714	-	-	-	Register – Address Failure 29;	As Ad	dr.574
715	0 – 9	-	-	Register – Status and Command of Equipment 30 to Parallelism;	As Add	dr. 570
716	50 – 150	-	-	Current TAP Position on Address 30;	R	- 100

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
717	50 – 150	-	-	Minimum TAP Position on Address 30;	R	- 100
718	50 – 150	_	-	Maximum TAP Position on Address 30;	R	- 100
719	50 – 150	-	-	Register – Address Failure 30;	As Ad	dr.574
720	-	-	-	Register – Status and Command of Equipment 31 to Parallelism;	As Ade	dr. 570
721	0 - 9	-	-	Current TAP Position on Address 31;	R	- 100
722	50 – 150	-	-	Minimum TAP Position on Address 31;	R	- 100
723	50 – 150	_	-	Maximum TAP Position on Address 31;	R	- 100
724	50 – 150	_	-	Register – Address Failure 31;	-	_
740	0 to 999.9	-	-	Voltage Reading on Primary da Phase A;	R	1:100
741	0 to 280	-	-	Voltage Reading on Secondary da Phase A;	R	1:100
742	0 to 999.9	-	-	Voltage Reading on Primary da Phase B;	R	1:100
743	0 to 280	-	-	Voltage Reading on Secondary da Phase B;	R	1:100
744	0 to 999.9	-	-	Voltage Reading on Primary da Phase C;	R	1:100
745	0 to 280	_	-	Voltage Reading on Secondary da Phase C;	R	1:100
746	0 to 999.9	_	-	Electrical Current Reading on Primary da Phase A;	R	1:100
747	0 to 280	_	-	Electrical Current Reading on Secondary da Phase A;	R	1:100
748	0 to 999.9	-	-	Electrical Current Reading on Primary da Phase B;	R	1:100
749	0 to 280	-	-	Electrical Current Reading on Secondary da Phase B;	R	1:100
750	0 to 999.9	_	-	Electrical Current Reading on Primary da Phase C;	R	1:100
751	0 to 10	_	_	Electrical Current Reading on Secondary da Phase C;	R	1:100
752	0 to 999.9	_	_	Apparent Power on Primary da Phase A;	R	1:100
753	0 to 999.9	_	-	Apparent Power on Secondary da Phase A;	R	1:100
754	0 to 999.9	_	-	Apparent Power on Primary da Phase B;	R	1:100
755	0 to 999.9	_	-	Apparent Power on Secondary da Phase B;	R	1:100
756	0 to 999.9	-	-	Apparent Power on Primary da Phase C;	R	1:100



SERIAL COMMUNICATION

MODBUS Reading Bits Write **Description / Point Name** Scale State Address Index Range Read 0 to 999.9 Apparent Power on Secondary da Phase C; 1:100 757 _ R _ 0 to 999.9 Active Power on Primary da Phase A; 758 _ R 1:100 759 0 to 999.9 Active Power on Secondary da Phase A; 1:100 R _ _ Active Power on Primary da Phase B; 760 0 to 999.9 R 1:100 _ — 761 0 to 999.9 Active Power on Secondary da Phase b; R 1:100 _ _ 762 Active Power on Primary da Phase C; 0 to 999.9 R 1:100 _ _ 763 0 to 999.9 Active Power on Secondary da Phase C; R 1:100 _ _ Reactive Power on Primary da Phase A; 0 to 999.9 764 R 1:100 _ — 765 0 to 999.9 Reactive Power on Secondary da Phase A; 1:100 R _ _ 766 0 to 999.9 Reactive Power on Primary da Phase B; 1:100 R _ Reactive Power on Secondary da Phase B; 767 0 to 999.9 _ _ R 1:100 Reactive Power on Primary da Phase C; 0 to 999.9 768 R 1:100 — Reactive Power on Secondary da Phase C; 769 0 to 999.9 R 1:100 _ _ 770 -1 to 1 Power Factor na Phase A; R -1:2000_ 771 -1 to 1 Power Factor na Phase B: R -1:2000_ _ Power Factor na Phase C; 772 -1 to 1 R -1:2000— 773 Network Frequency Oscilation (Hz); R 1:100 -1 to 1 _ _ Register – Signal Value of actual voltage deviation on Phase A; _ 774 0 0 Actual Phase A positive Voltage Deviation; R _ Actual Phase A negative Voltage Deviation; 0 R 1 775 _ Actual Phase Voltage Deviation A; R 1:10 _ _ Register – Voltage Value Deviation Signal Calculated on Phase A; _ 776 Voltage Deviation Calculated on Phase A positive; 0 R _ 0 Voltage Deviation Calculated on Phase B negative; R 1 _ 777 _ _ _ Voltage Deviation Calculated on Phase A; R 1:10

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale	
		_		Register – Voltage Value Deviation Signal Calculated on Phase B;	-	_	
778	-	0	0	Actual Phase Voltage Deviation B positive;	R	-	
		0	1	Actual Phase Voltage Deviation B negative;	R	-	
779	-	-	-	Actual Phase Voltage Deviation B;	R	-	
		-		Register – Voltage Value Deviation Signal Calculated on Phase B;	-	-	
780	-	0	0	Positive Phase A Calculated Voltage Deviation	R	-	
			0	1	Negative Phase A Calculated Voltage Deviation	R	-
781	-	-	-	Compensated Voltage Deviation on Phase B;	R	1:10	
		_		Register – Real Voltage Deviation Signal Calculated on Phase C;	-	_	
782	-	0	0	Actual Phase Voltage Deviation C positive;	R	-	
		0	1	Actual Phase Voltage Deviation C negative;	R	-	
783	-	-	-	Actual Phase Voltage Deviation C;	R	1:10	
		-		Register – Voltage Value Deviation Signal Calculated on Phase C;	-	-	
784	-	0	0	Voltage Deviation Calculated on Phase C positive;	R	-	
		0	1	Voltage Deviation Calculated on Phase C negative;	R	-	
785	-	-	-	Voltage Deviation Calculated on Phase C;	R	1:10	
786	0 to 280	-	-	Compensated Voltage Phase A;	R	1:10	
787	0 to 280	-	-	Compensated Voltage Phase B;	R	1:10	
788	0 to 280	-	-	Compensated Voltage Phase C;	R	1:10	
789	0 to 9999	-	-	Winding Load Percentage on Phase A;	R	1:10	
790	0 to 9999	-	-	Winding Load Percentage on Phase B;	R	1:10	
791	0 to 9999	-	_	Winding Load Percentage on Phase C;	R	1:10	
792	0 to 280	-	_	Regulation Voltage Selected;	R	1:10	
793	1 to 8	_	-	Regulation Set Selected;	R	1:1	
795	50 - 150	_	_	Current TAP Position;	R	-100	

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
796	50 - 150	_	_	Min TAP Position;	R	-100
797	50 – 150	-	-	Max TAP Position;	R	-100
798	-	-	-	Min and Max TAP Reset Commands;	-	-
_	-	-	-	Register – Total Commutation Numbers Performed by the TAP Changer:	-	_
799	0 to 16x10 ⁶	-	-	Commutation Number performed by the TAP Changer – LSB;	W/R	1:1
800	0 10 10/10	-	-	Total Commutation Numbers Performed by the TAP Changer– MSB;	VV / IX	1.1
_	-	_	_	Reset Commands – Commutation Numbers Performed after TAP Changer Maintenance;	-	-
801	0 to 16x10 ⁶	-	-	Total Commutations Number performed by the TAP Changer – LSB	W/R	1:100
802	0 10 10/10	-	-	Total Commutations Number performed by the TAP Changer – MSB;	VV / IX	1.100
_	-	-	-	Register – Squared Current Sum since its last Maintenance:	-	_
803	-	-	-	Squared Current Sum since its last Maintenance – MSB;	W/R	1:100
804	-	-	-	Squared Current Sum since its last Maintenance – LSB;	VV / IX	1.10
_	-	-	-	Register – Total Squared Current Sum:	-	-
805	0.00 to		_	Total Squared Current Sum – LSB;	W/R	1:10
806	99999.99	_	_	Total Squared Current Sum – MSB;	W/R	1.10
_	-	_	_	Register – Daily Average of TAP Changer Operations.	-	-
807	0 to 999999	-	-	Daily Average of TAP Changer Operations – LSB;	W/R	1:1
808	0 10 3333333	-	-	Daily Average of TAP Changer Operations – MSB;	W/R	1.1
_	-	_	_	Register – Weekly Average of TAP Changer Operations.	-	_
809	0 to 999999	_	_	Weekly Average of TAP Changer Operations – LSB;	W/R	1:1
810	010333399	_	-	Weekly Average of TAP Changer Operations – MSB;	W/R	1.1
_	-	_	_	Register – Monthly Average of TAP Changer Operations –;	-	_
811	0 to 99999	-	-	Monthly Average of TAP Changer Operations – LSB;	W/R	1:1
812	0 10 55555	_	_	Monthly Average of TAP Changer Operations – MSB;	W/R	1.1

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MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
_	_	_	_	Register – Quarterly Average of TAP Changer Operations:	-	-
813	0 to 99999	-	-	Quarterly Average of TAP Changer Operations – LSB;	W / R	1:1
814	0 10 99999	-	-	Quarterly Average of TAP Changer Operations – MSB;	W/R	1.1
_	-	-	-	Register – Semesterly Average of TAP Changer Operations.	-	-
815	0 to 99999	_		Semesterly Average of TAP Changer Operations – LSB;	W/R	1:1
816	0 10 99999	_		Semesterly Average of TAP Changer Operations – MSB;	W/R	1.1
_	-	-		Register – Annual Average of TAP Changer Operations:	-	-
817	0.00 to	-		Annual Average of TAP Changer Operations – LSB;	W/R	1:1
818	99999.9	-		Annual Average of TAP Changer Operations – MSB;	W/R	1.1
_	_	_		Register – Daily Average of Electrical Current Sum:	_	_
819	0.00 to	_		Daily Average of Electrical Current Sum – LSB;	W/R	1:10
820	99999.9	_		Daily Average of Electrical Current Sum – MSB;	W/R	1.10
_	_	-		Register – Weekly Average of Squared Current Sum:	-	-
821	0.00 to	_		Weekly Average of Squared Current Sum – LSB;	W/R	1:10
822	99999.9	_		Weekly Average of Squared Current Sum – MSB.	W/R	1.10
	-	-		Register – Monthly Average of Squared Current Sum;	-	
823	0.00 to			Monthly Average of Squared Current Sum – LSB;	W/R	1:10
824	99999.9	_		Monthly Average of Squared Current Sum – MSB;	W/R	1.10
_	-	-		Register – Quarterly Average of Squared Current Sum;	-	_
825	0.00 to	_		Quarterly Average of Squared Current Sum – LSB;	W / R	1:10
826	99999.9	_		Quarterly Average of Squared Current Sum – MSB;	W/R	1.10
	_	-		Register – Semesterly Average of Squared Current Sum:	-	-
827	0.00 to	_		Semesterly Average of Squared Current Sum – LSB;	R	1:10
828	99999.9			Semesterly Average of Squared Current Sum – MSB;	R	1.10
-	-	_		Register – Annual Average of Squared Current Sum;	_	_

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
829	0.00 to	_	_	Annual Average of Squared Current Sum – LSB;	R	1:10
830	99999.9	-	-	Annual Average of Squared Current Sum – MSB;	R	1.10
831	0 – 59	-	-	TAP Changer Partial Hour Meter – Minutes;	W / R	1:1
832	0-23	-	-	TAP Changer Partial Hour Meter – Hours;	W / R	1:1
833	0 – 365	-	-	TAP Changer Partial Hour Meter – Days;	W / R	1:1
834	0 – 365	-	-	TAP Changer Partial Hour Meter – Years;	W / R	1:1
835	0 – 59	_	_	TAP Changer Total Hour Meter – Minutes;	W / R	1:1
836	0-23	_	_	TAP Changer Total Hour Meter – Hours;	W / R	1:1
837	0 - 365	_	_	TAP Changer Total Hour Meter – Days;	W / R	1:1
838	0 - 365	_	_	TAP Changer Total Hour Meter – Years;	W / R	1:1
_	_	_	_	Register – First Maintenance Historic – Commutations Number;	-	_
839	0 to 99999	_	-	First Maintenance Historic – Commutations Number– LSB;	R	1:1
840	010999999	_	-	First Maintenance Historic – Commutations Number– MSB;	R	1:1
_	-	_	_	Register – Second Maintenance Historic – Commutations Number;	-	_
841	0 to 99999	_	_	Second Maintenance Historic – Commutations Number– LSB;	R	1:1
842	0 10 999999	_	_	Second Maintenance Historic – Commutations Number– MSB;	R	1:11
-	-	_	-	Register – Third Maintenance Historic – Commutations Number;	-	-
843	0 to 99999	_	-	Third Maintenance Historic – Commutations Number– LSB;	R	1:1
844	010999999	_	_	Third Maintenance Historic – Commutations Number– MSB;	R	1:1
_	-	_	_	Register – Fourth Maintenance Historic – Commutations Number;	-	_
845	0 to 99999	_	_	Fourth Maintenance Historic – Commutations Number– LSB;	R	1:1
846	0 10 99999	_	_	Fourth Maintenance Historic – Commutations Number– MSB;	R	1:1
_	_	_	_	Register – Fifth Maintenance Historic – Commutations Number;	-	_
847	0 to 99999	_	-	Fifth Maintenance Historic – Commutations Number– LSB;	R	1:1
848	0 10 99999	_	_	Fifth Maintenance Historic – Commutations Number– MSB;	R	1:1

Electron

MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
_	_	-	_	Register – First Maintenance Historic – Electrical Current Sum	-	-
849	0 to 99999.9	-	-	First Maintenance Historic – Electrical Current Sum – LSB;	R	1:10
850	0 10 99999.9	_	-	First Maintenance Historic – Electrical Current Sum – MSB;	R	1:10
_	-	-	-	Register – Second Maintenance Historic – Electrical Current Sum:	-	-
851	0 to 99999.9	-	-	Second Maintenance Historic – Electrical Current Sum – LSB;	R	1:10
852	0 10 99999.9	_	-	Second Maintenance Historic – Electrical Current Sum – MSB;	R	1:10
_	-	-	-	Register – Third Maintenance Historic – Electrical Current Sum	-	-
853	0 to 99999.9	_	_	Third Maintenance Historic – Electrical Current Sum – LSB;	R	1:10
854	0 10 99999.9	-	-	Third Maintenance Historic – Electrical Current Sum – MSB;	R	1:10
_				Register – Fourth Maintenance Historic – Electrical Current Sum	-	_
855	0 to 99999.9	-	-	Fourth Maintenance Historic – Electrical Current Sum – LSB;	R	1:10
856	0 10 99999.9	_	-	Fourth Maintenance Historic – Electrical Current Sum – MSB;	R	1:10
_	-	-	-	Register – Fourth Maintenance Historic – Electrical Current Sum	-	
857	0 to 99999.9	-	-	Fifth Maintenance Historic – Electrical Current Sum – LSB;	R	1:10
858	0 10 99999.9	-	-	Fifth Maintenance Historic – Electrical Current Sum – MSB;	R	1:10
859	1 – 31	-	-	First Maintenance Historic – Days;	-	_
860	1 – 31	-	-	Second Maintenance Historic – Days;	R	1:1
861	1 – 31	-	-	Third Maintenance Historic – Days;	R	1:1
862	1-31	_	_	Fourth Maintenance Historic – Days;	R	1:1
863	1-31	_	_	Fifth Maintenance Historic – Diays;	R	1:1
864	1 – 12	_	_	First Maintenance Historic – Month;	R	1:1
865	1 – 12	_	-	Second Maintenance Historic – Month;	R	1:1
866	1 – 12	-	-	Third Maintenance Historic – Month;	R	1:1
867	1 – 12	-	-	Fourth Maintenance Historic – Month;	R	1:1
868	1-12	_	_	Fifth Maintenance Historic – Month;	R	1:1

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MODBUS Address	Reading Range	Bits Index	State	Description / Point Name	Write Read	Scale
869	0 – 9999	-	_	First Maintenance Historic – Year;	R	1:1
870	0 – 9999	-	-	Second Maintenance Historic – Year;	R	1:1
871	0 – 9999	-	-	Third Maintenance Historic – Year;	R	1:1
872	0 – 9999	-	-	Fourth Maintenance Historic – Year;	R	1:1
873	0 – 9999	-	-	Fifth Maintenance Historic – Year;	R	1:1
874	0 – 59	-	-	First Maintenance Historic – Minute;	R	1:1
875	0 – 59	-	_	Second Maintenance Historic – Minute;	R	1:1
876	0 – 59	-	-	Third Maintenance Historic – Minute;	R	1:1
877	0 – 59	-	_	Fourth Maintenance Historic – Minute;	R	1:1
878	0 – 59	_	_	Fifth Maintenance Historic – Minute;	R	1:1
879	0 - 23	_	_	First Maintenance Historic – Hour;	R	1:1
880	0 - 23	_	_	Second Maintenance Historic – Hour;	R	1:1
881	0 – 23	_	_	Third Maintenance Historic – Hour;	R	1:1
882	0 - 23	-	_	Fourth Maintenance Historic – Hour;	R	1:1
883	0 - 23	-	_	Fifth Maintenance Historic – Hour;	R	1:1
884				-		
885	-	-	-	Relay 1 Drive signaling;	R	-
886	-	-	-	Relay 2 Drive signaling;	R	-
887	_	-	_	Relay 3 Drive signaling;	R	_
888	-	_	_	Relay 4 Drive signaling;	R	_
889	-	_	_	Relay 5 Drive signaling;	R	_
890	-	-	_	Relay 6 Drive signaling;	R	_
891	-	-	_	Relay 7 Drive signaling;	R	_
892	-	-	_	Relay 8 Drive signaling;	R	_
893	_	_	_	Relay 9 Drive signaling;	R	_

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SERIAL COMMUNICATION

MODBUS Reading Bits Write **Description / Point Name** State Scale Address Range Index Read Relay 10 Drive signaling; 894 R _ _ _ _ 895 Relay 11 Drive signaling; R _ _ _ _ 896 _ Relay 12 Drive signaling; R _ _ _ Relay 13 Drive signaling; 897 _ R _ _ _ Relay 14 Drive signaling; 898 R _ _ _ _ _ Register – Blocakage Condition: _ Undervoltage; 0 1 R _ _ Overvoltage; 1 1 R — — Overcurrent; 2 1 R _ _ 899 3 1 Reverse Current; R _ _ 4 TAP Changer Failure; 1 R _ _ 5 Compensation Failure; 1 R _ — 6 1 Regulation Failure; R _ _



SERIAL COMMUNICATION – RELAY SIGNALING

Bits Index	State	Description / Point Name	Write Read
		Relay Signaling – Function Command	
0	1	Lower Voltage Command;	-
1	1	Raise Voltage Command;	—
		Relay Signaling – Alarm Function	
0	1	Alarm by Undervoltage;	-
1	1	Alarm by Overvoltage;	-
2	1	Alarm by Overcurrent;	-
5	1	TAP Changer Maintenance;	-
6	1	Compensation Alarm;	—
7	1	Reverse Current Alarm;	—
		Relay Signaling – Failure Function	
2	1	TAP Changer Failure;	-
6	1	TAP Changer Maintenance;	-
7	1	Max Failure Compensation;	_
8	1	Reverse Current Failure;	_
9	1	Regulation Failure;	-

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SERIAL COMMUNICATION - DISPLAY QUANTITY PRESENTATION

3Displays Phase B Secondary Voltage;V4Displays Phase B Primary Voltage;KV5Displays Phase C Secondary Voltage;V6Displays Phase C Primary Voltage;KV7Displays Phase A Electrical Current on Secondary;A8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Primary;KA10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Secondary;A12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase B Apparent Power on Primary;MV15Displays Phase C Apparent Power on Primary;MV16Displays Phase C Apparent Power on Primary;WV17Displays Phase C Apparent Power on Primary;MV18Displays Phase C Apparent Power on Primary;WV20Displays Phase B Active Power on Primary;MV21Displays Phase B Active Power on Primary;MV22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W23Displays Phase C Active Power on Secondary;W	Value	Display Quantity Presentation	Unit
2Displays Phase A Primary Voltage;KV3Displays Phase B Secondary Voltage;V4Displays Phase B Primary Voltage;KV5Displays Phase C Secondary Voltage;V6Displays Phase C Primary Voltage;KV7Displays Phase A Electrical Current on Secondary;A8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Primary;A10Displays Phase B Electrical Current on Primary;KA11Displays Phase B Electrical Current on Primary;KA12Displays Phase C Electrical Current on Primary;KA13Displays Phase C Electrical Current on Primary;KA14Displays Phase A Apparent Power on Secondary;VA15Displays Phase B Apparent Power on Primary;MV16Displays Phase C Apparent Power on Primary;MV17Displays Phase C Apparent Power on Primary;MV18Displays Phase A Active Power on Secondary;W20Displays Phase B Active Power on Primary;MV21Displays Phase B Active Power on Primary;MV22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W23Displays Phase C Active Power on Secondary;W	0	Displays Current TAP Position;	-
3Displays Phase B Secondary Voltage;V4Displays Phase B Primary Voltage;KV5Displays Phase C Secondary Voltage;V6Displays Phase C Primary Voltage;KV7Displays Phase A Electrical Current on Secondary;A8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Primary;KA10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Primary;KA12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase B Apparent Power on Primary;MV15Displays Phase C Apparent Power on Primary;MV16Displays Phase C Apparent Power on Primary;MV17Displays Phase C Apparent Power on Primary;MV18Displays Phase A Active Power on Primary;MV20Displays Phase B Active Power on Secondary;W21Displays Phase B Active Power on Primary;MV22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W23Displays Phase C Active Power on Secondary;W	1	Displays Phase A Secondary Voltage;	V
4Displays Phase B Primary Voltage;KV5Displays Phase C Secondary Voltage;V6Displays Phase C Primary Voltage;KV7Displays Phase A Electrical Current on Secondary;A8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Secondary;A10Displays Phase B Electrical Current on Primary;KA11Displays Phase B Electrical Current on Primary;KA12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase B Apparent Power on Primary;MV15Displays Phase B Apparent Power on Primary;MV16Displays Phase C Apparent Power on Primary;MV17Displays Phase C Apparent Power on Primary;MV18Displays Phase A Active Power on Primary;MV20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Primary;MV22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W23Displays Phase C Active Power on Secondary;W	2	Displays Phase A Primary Voltage;	KV
5Displays Phase C Secondary Voltage;V6Displays Phase C Primary Voltage;KV7Displays Phase A Electrical Current on Secondary;A8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Secondary;A10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Primary;KA12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase B Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase C Apparent Power on Primary;MV17Displays Phase A Active Power on Primary;MV19Displays Phase A Active Power on Primary;MV20Displays Phase B Active Power on Primary;MV21Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W23Displays Phase C Active Power on Secondary;W	3	Displays Phase B Secondary Voltage;	V
6Displays Phase C Primary Voltage;KV7Displays Phase A Electrical Current on Secondary;A8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Secondary;A10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Primary;KA12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase A Apparent Power on Secondary;VA15Displays Phase B Apparent Power on Primary;MV16Displays Phase C Apparent Power on Primary;MV17Displays Phase C Apparent Power on Primary;MV18Displays Phase A Active Power on Primary;MV20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Primary;MV22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	4	Displays Phase B Primary Voltage;	KV
7Displays Phase A Electrical Current on Secondary;A8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Secondary;A10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Secondary;A12Displays Phase C Electrical Current on Primary;KA13Displays Phase C Electrical Current on Primary;KA14Displays Phase A Apparent Power on Secondary;VA15Displays Phase B Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase C Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Primary;MV20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Primary;MV22Displays Phase C Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	5	Displays Phase C Secondary Voltage;	V
8Displays Phase A Electrical Current on Primary;KA9Displays Phase B Electrical Current on Secondary;A10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Secondary;A12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase A Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	6	Displays Phase C Primary Voltage;	KV
9Displays Phase B Electrical Current on Secondary;A10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Secondary;A12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase A Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Primary;MV22Displays Phase B Active Power on Secondary;W23Displays Phase C Active Power on Secondary;W	7	Displays Phase A Electrical Current on Secondary;	A
10Displays Phase B Electrical Current on Primary;KA11Displays Phase C Electrical Current on Secondary;A12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase A Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Primary;MV20Displays Phase B Active Power on Secondary;W21Displays Phase B Active Power on Primary;MV22Displays Phase B Active Power on Secondary;W23Displays Phase C Active Power on Secondary;W	8	Displays Phase A Electrical Current on Primary;	KA
11Displays Phase C Electrical Current on Secondary;A12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase A Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Primary;W22Displays Phase B Active Power on Primary;W23Displays Phase C Active Power on Secondary;W	9	Displays Phase B Electrical Current on Secondary;	A
12Displays Phase C Electrical Current on Primary;KA13Displays Phase A Apparent Power on Secondary;VA14Displays Phase A Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Primary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	10	Displays Phase B Electrical Current on Primary;	КА
13Displays Phase A Apparent Power on Secondary;VA14Displays Phase A Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	11	Displays Phase C Electrical Current on Secondary;	A
14Displays Phase A Apparent Power on Primary;MV15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	12	Displays Phase C Electrical Current on Primary;	КА
15Displays Phase B Apparent Power on Secondary;VA16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	13	Displays Phase A Apparent Power on Secondary;	VA
16Displays Phase B Apparent Power on Primary;MV17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	14	Displays Phase A Apparent Power on Primary;	MVA
17Displays Phase C Apparent Power on Secondary;VA18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	15	Displays Phase B Apparent Power on Secondary;	VA
18Displays Phase C Apparent Power on Primary;MV19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	16	Displays Phase B Apparent Power on Primary;	MVA
19Displays Phase A Active Power on Secondary;W20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	17	Displays Phase C Apparent Power on Secondary;	VA
20Displays Phase A Active Power on Primary;MV21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	18	Displays Phase C Apparent Power on Primary;	MVA
21Displays Phase B Active Power on Secondary;W22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	19	Displays Phase A Active Power on Secondary;	W
22Displays Phase B Active Power on Primary;MV23Displays Phase C Active Power on Secondary;W	20	Displays Phase A Active Power on Primary;	MW
23 Displays Phase C Active Power on Secondary; W	21	Displays Phase B Active Power on Secondary;	W
	22	Displays Phase B Active Power on Primary;	MW
24Displays Phase C Active Power on Primary;MV	23	Displays Phase C Active Power on Secondary;	W
	24	Displays Phase C Active Power on Primary;	MW
25 Displays Phase A Reactive Power on Secondary; Va	25	Displays Phase A Reactive Power on Secondary;	Var
26 Displays Phase A Active Power on Primary; MVa	26	Displays Phase A Active Power on Primary;	MVar
27 Displays Phase B Reactive Power on Secondary; Va	27	Displays Phase B Reactive Power on Secondary;	Var
28 Displays Phase B Active Power on Primary; MVa	28	Displays Phase B Active Power on Primary;	MVar

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SERIAL COMMUNICATION - DISPLAY QUANTITY PRESENTATION

Value	Display Quantity Presentation	Unit
29	Displays Phase C Reactive Power on Secondary;	Var
30	Displays Phase C Active Power on Primary;	MVar
31	Displays Phase A Power Factor;	-
32	Displays Phase B Power Factor;	-
33	Displays Phase C Power Factor;	-
34	Displays Phase A Compensated Voltage;	V
35	Displays Phase A Voltage Deviation;	%
36	Displays Phase A Compensated Voltage Deviation;	%
37	Displays Phase B Compensated Voltage;	V
38	Displays Phase B Voltage Deviation;	%
39	Displays Compensated Voltage Deviation;	%
40	Displays Phase C Compensated Voltage;	V
41	Displays Phase C Voltage Deviation;	%
42	Displays Phase C Compensated Voltage Deviation;	%
43	Displays Reference Voltage;	V
44	Displays Line Frequency;	Hz
45	Displays Phase A Load Percentage;	%
46	Displays Phase B Load Percentage;	%
47	Displays Phase C Load Percentage;	%
48	Displays Nothing;	-